

Figure 57

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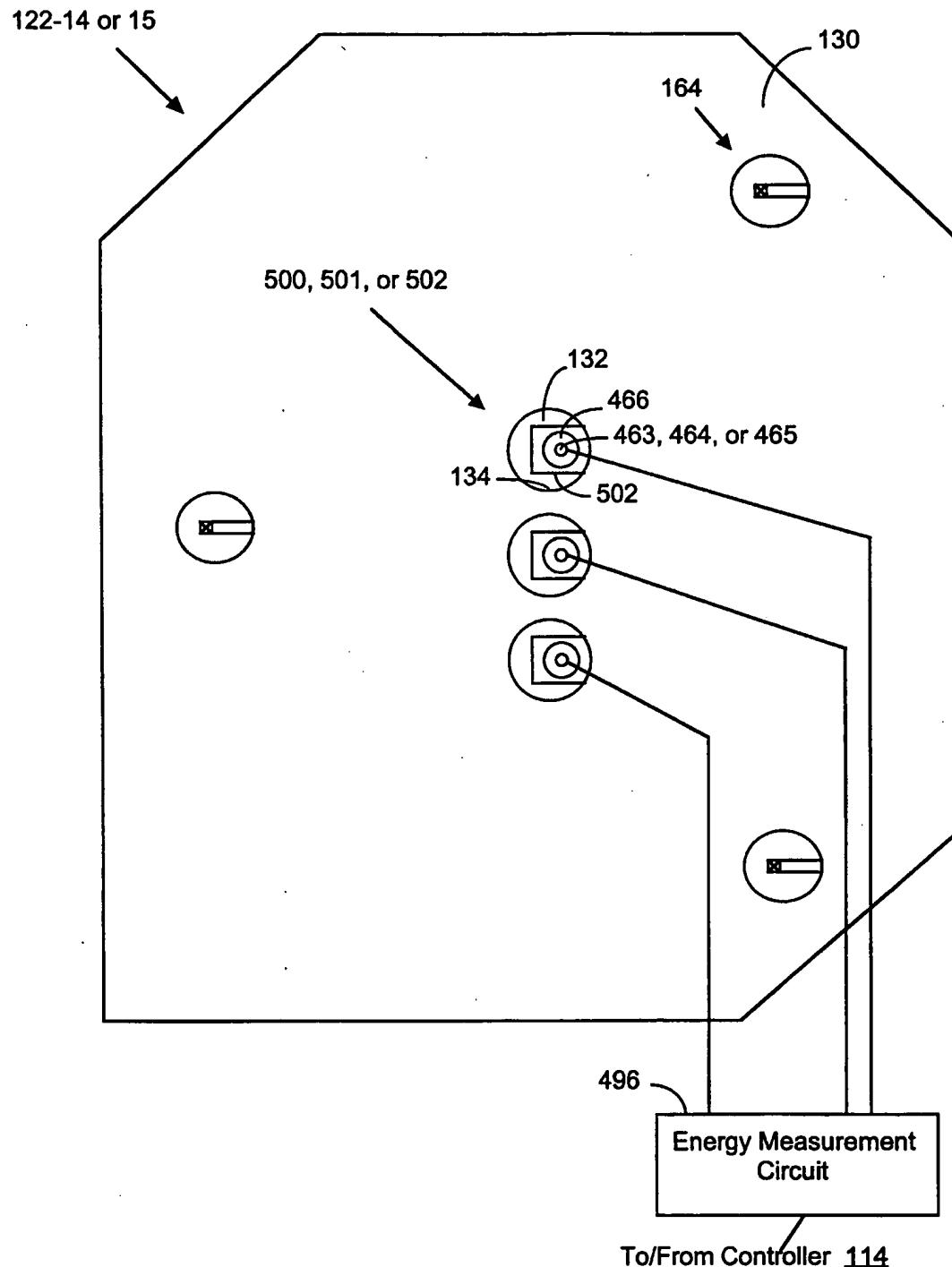


Figure 58

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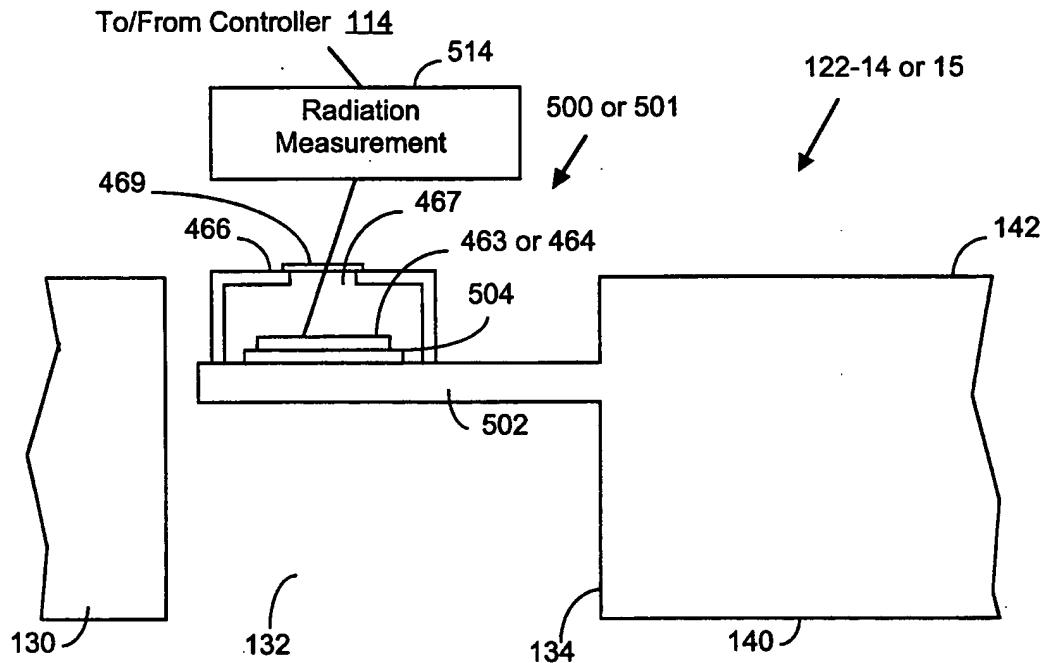


Figure 59

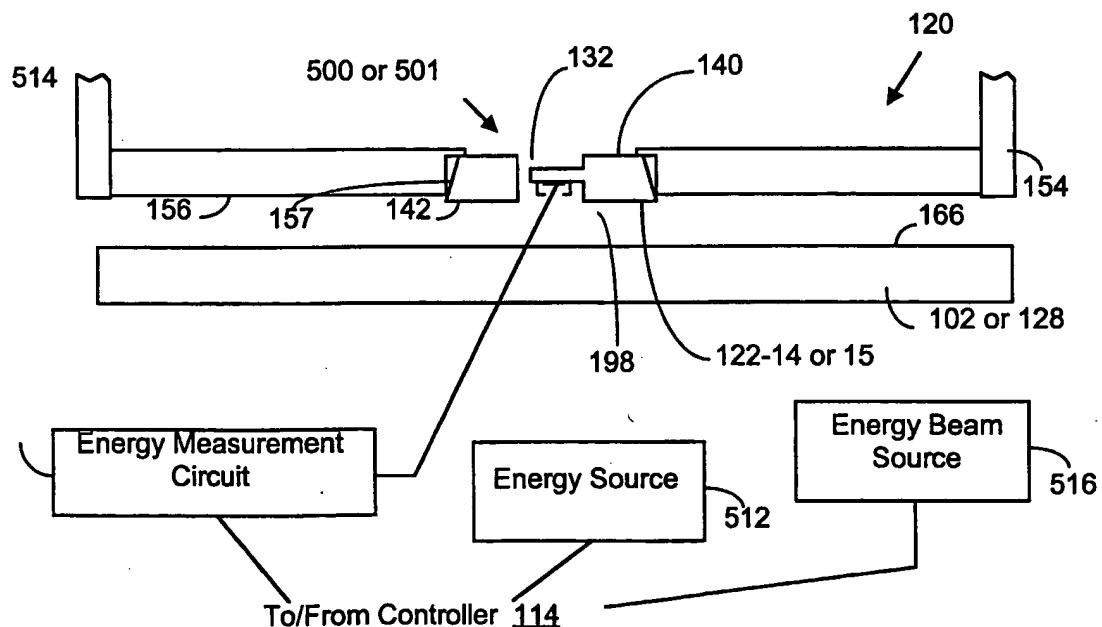


Figure 60

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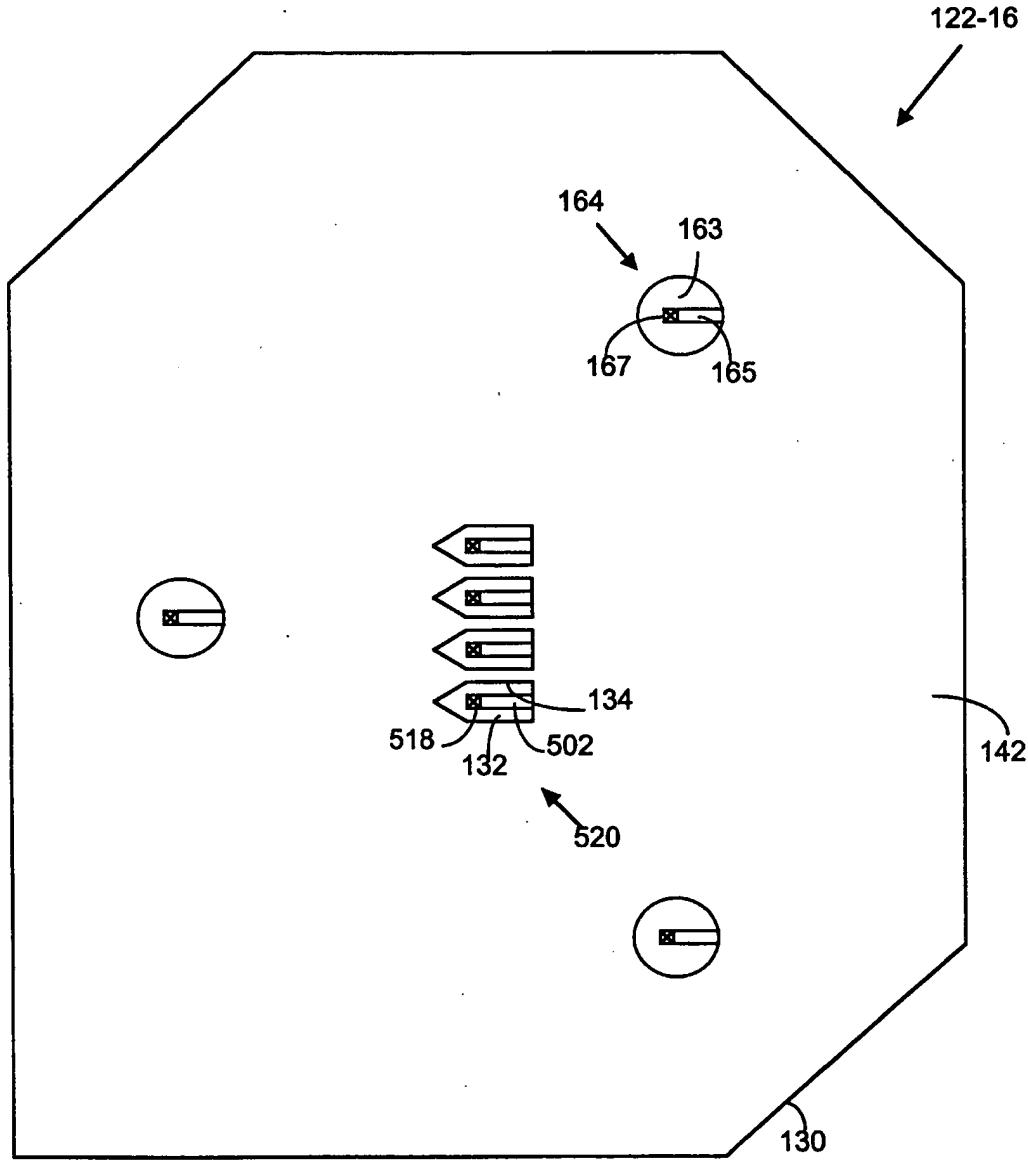


Figure 61

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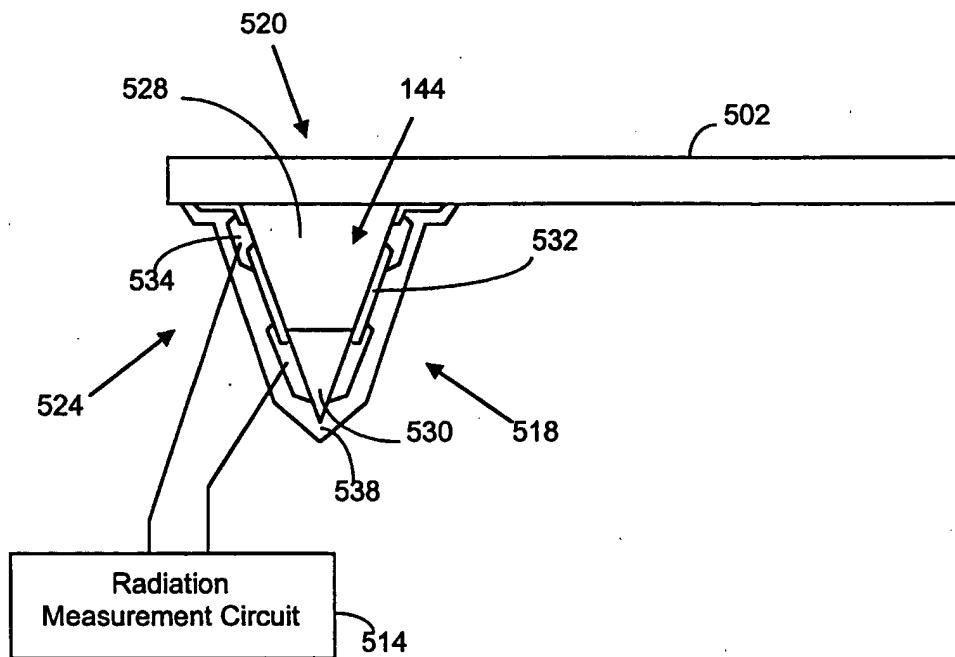


Figure 62

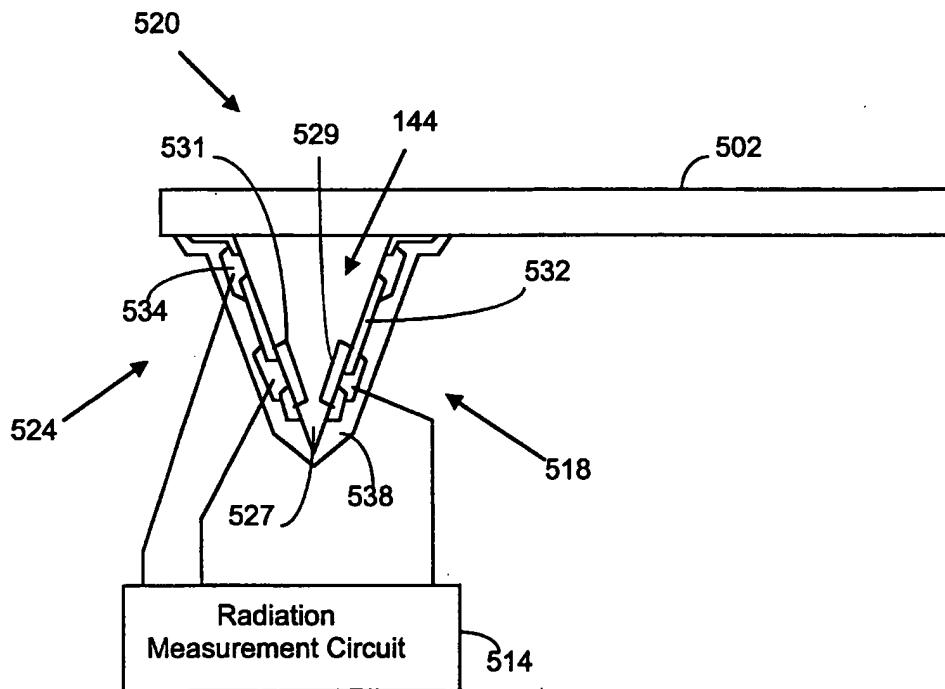


Figure 63

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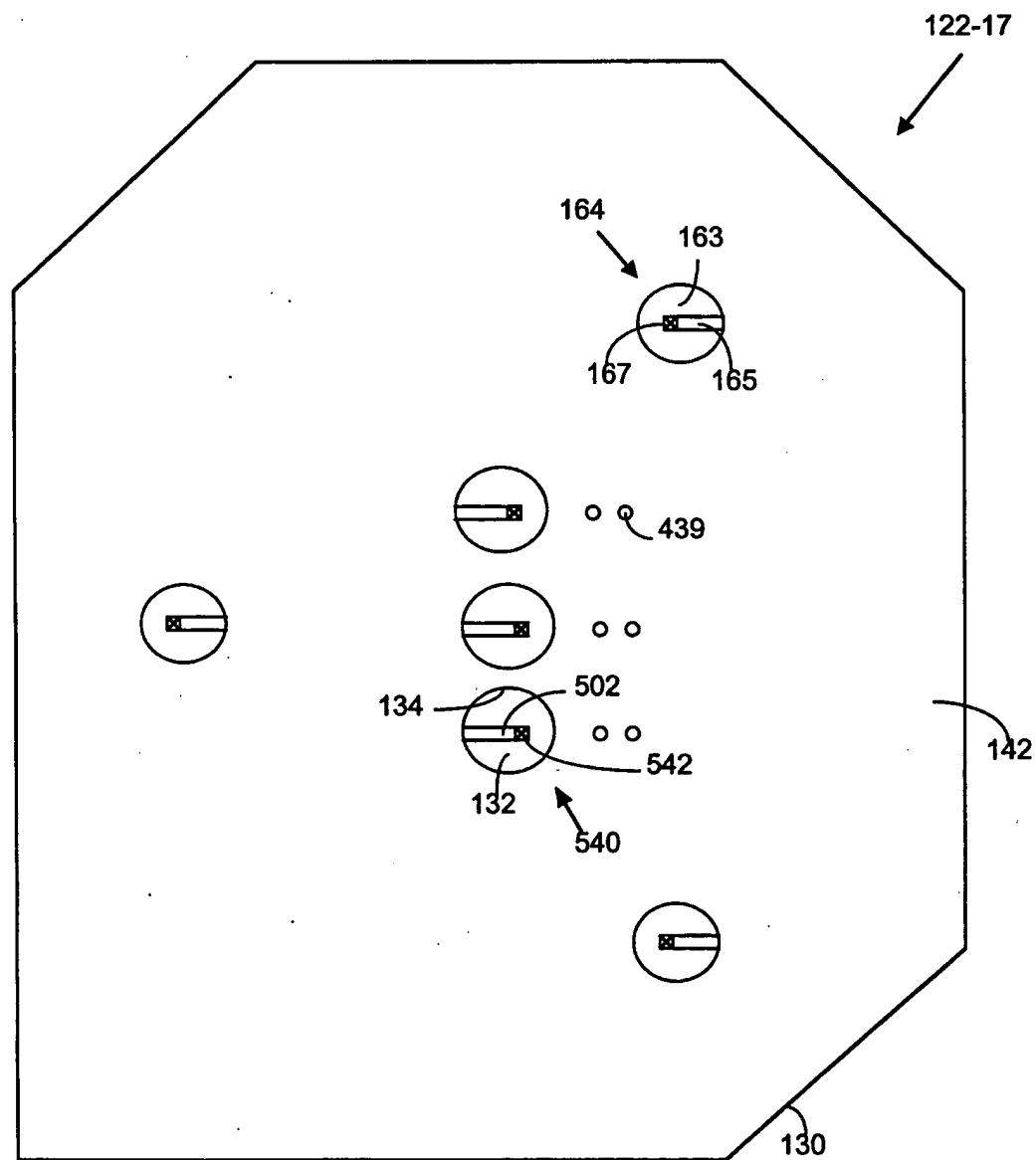


Figure 64

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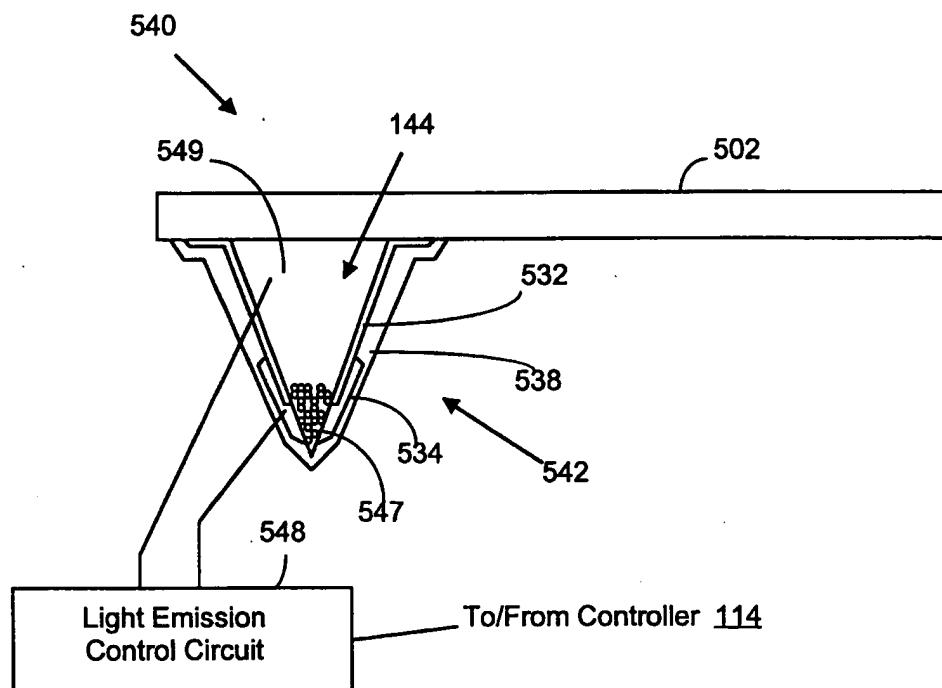


Figure 66

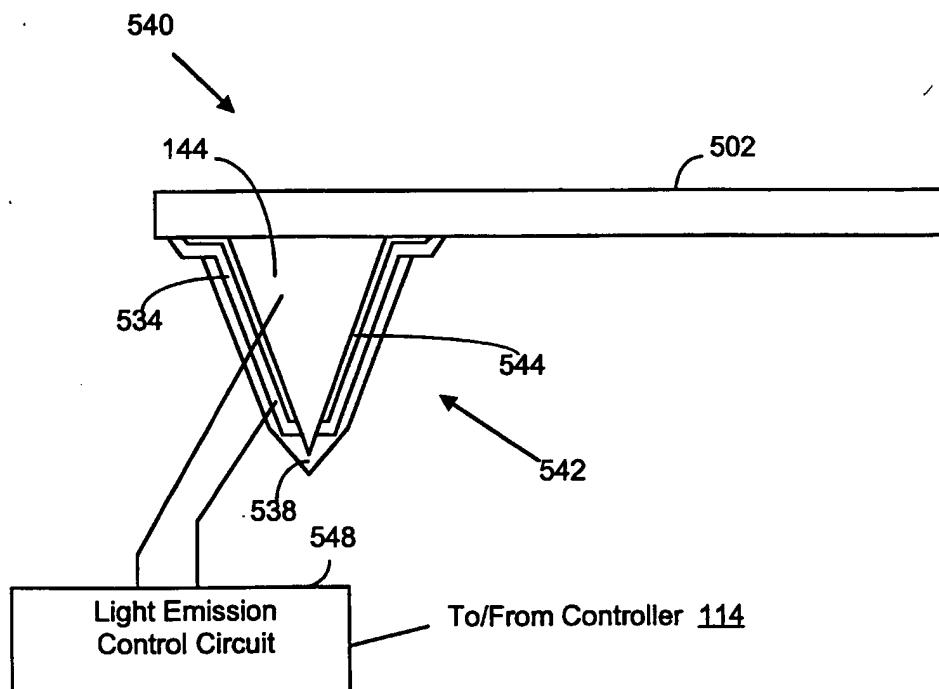


Figure 65

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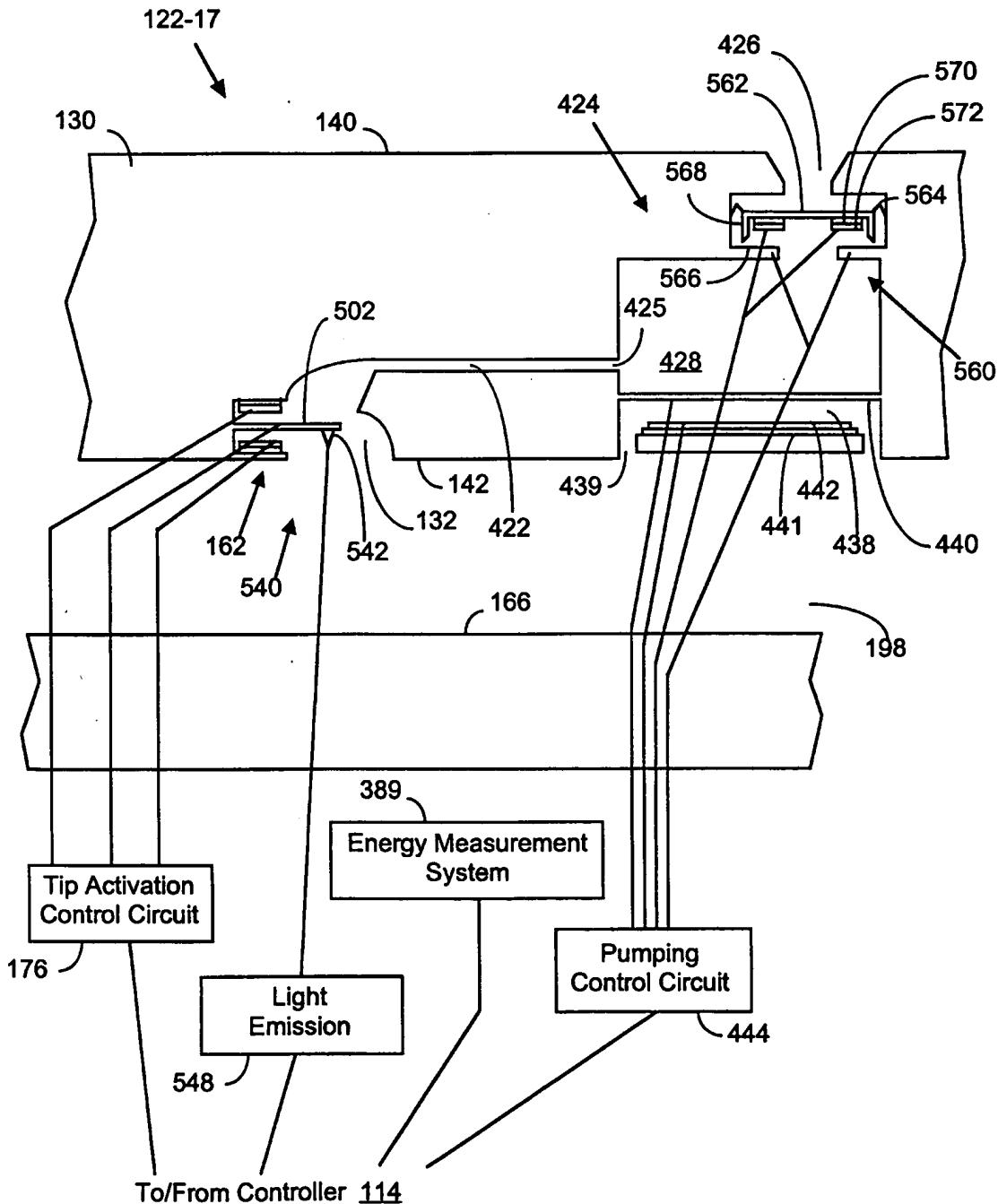


Figure 67

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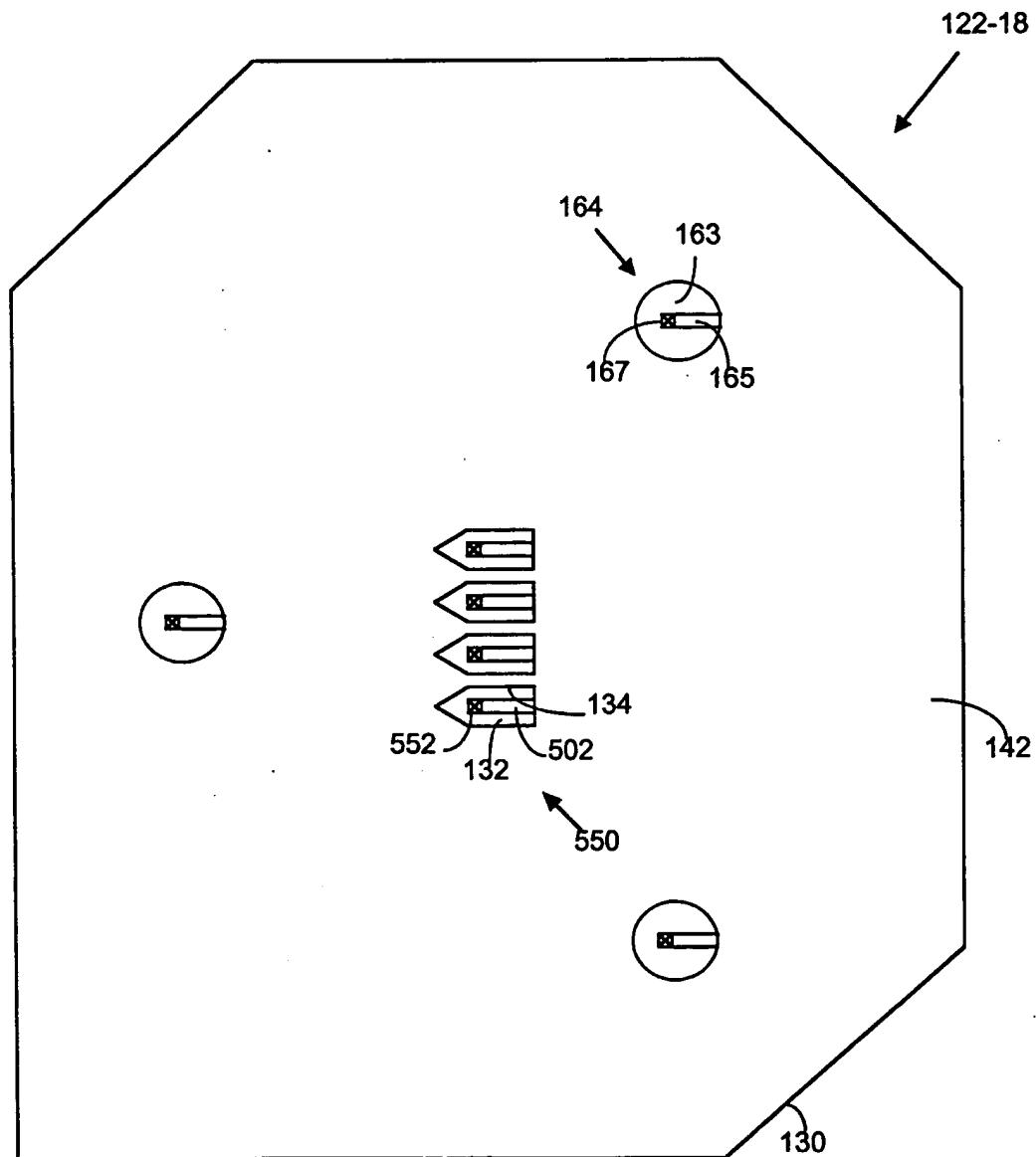


Figure 68

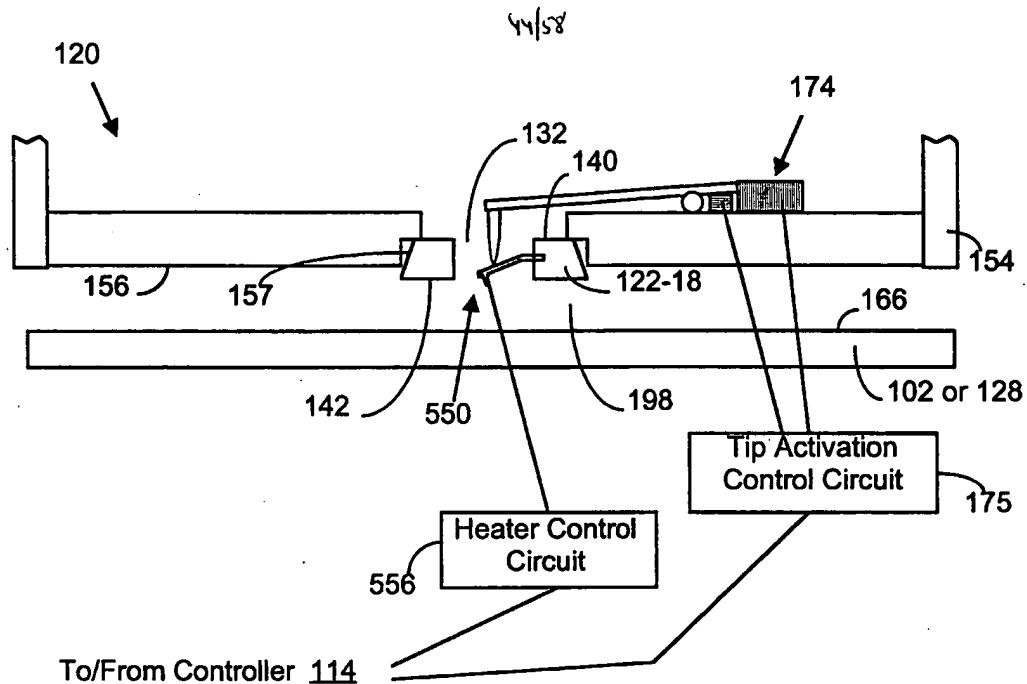


Figure 70

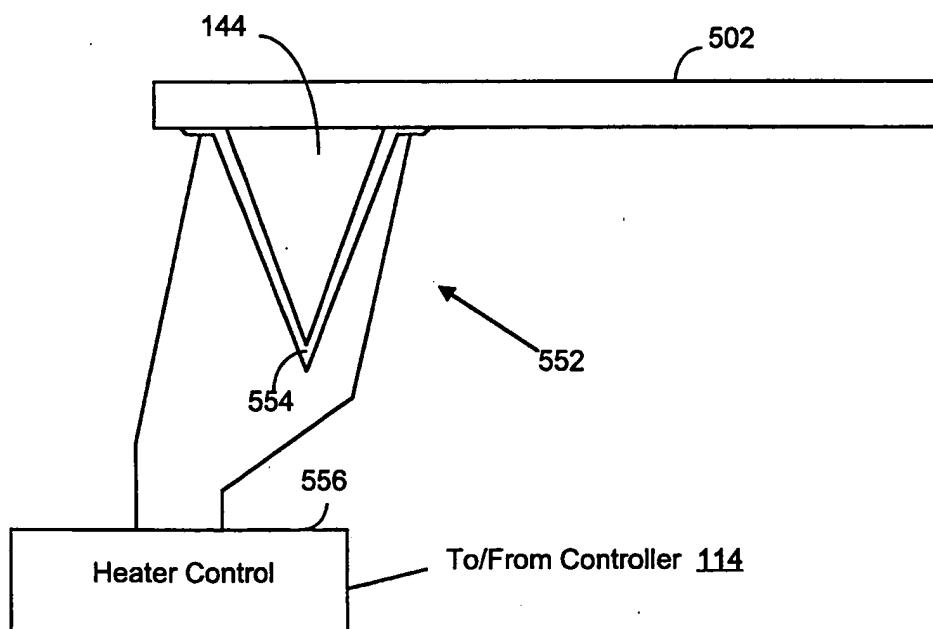


Figure 69

45|58

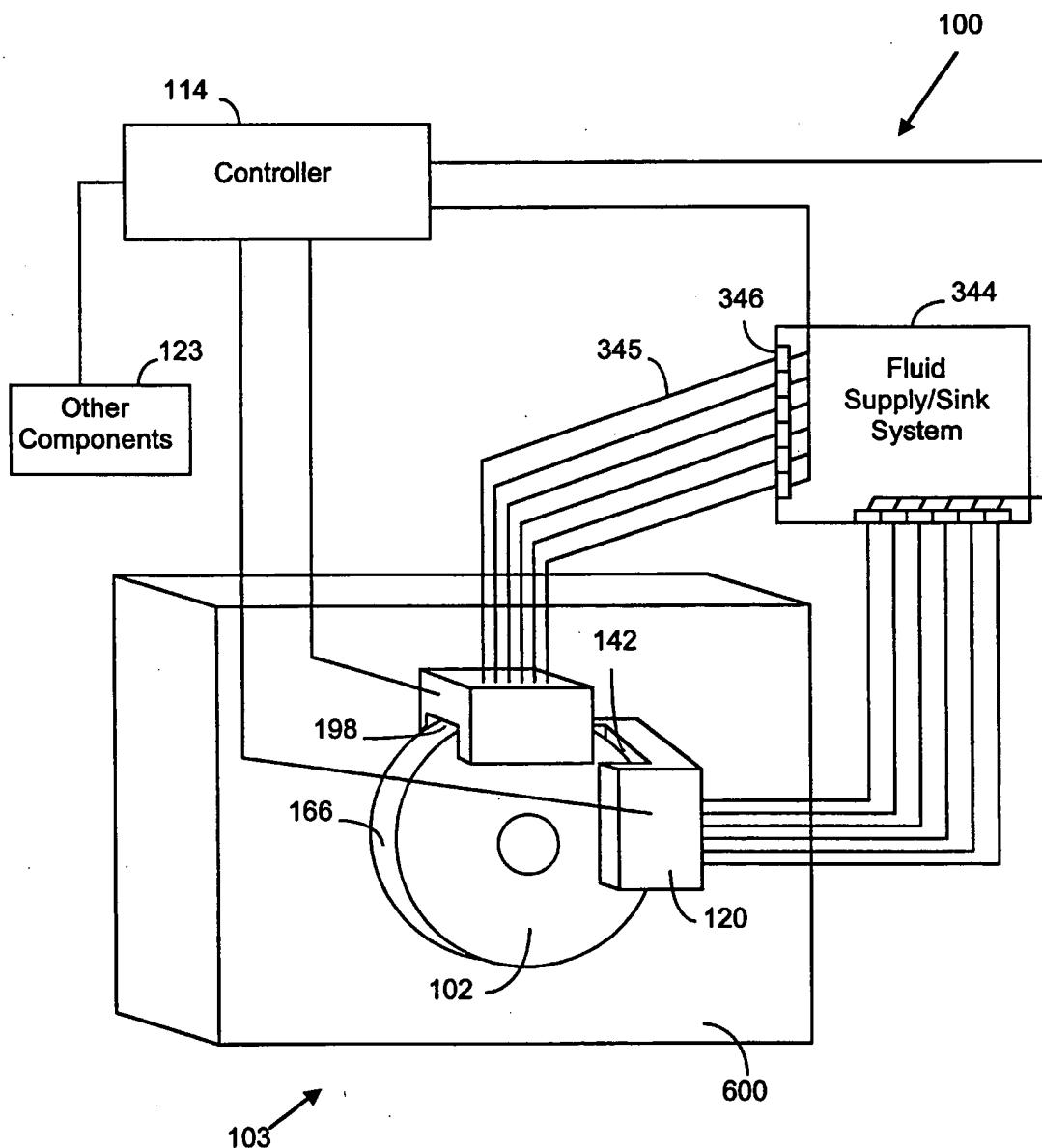


Figure 71

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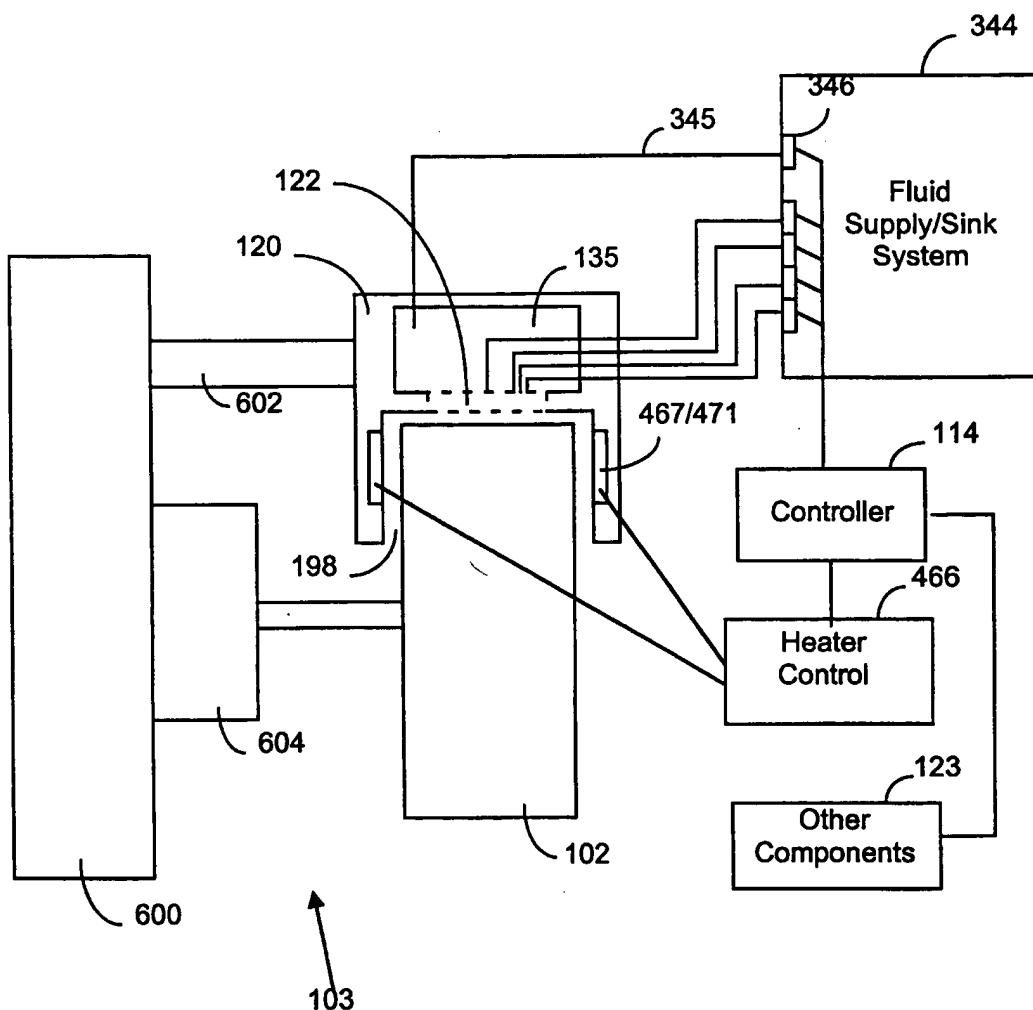


Figure 72

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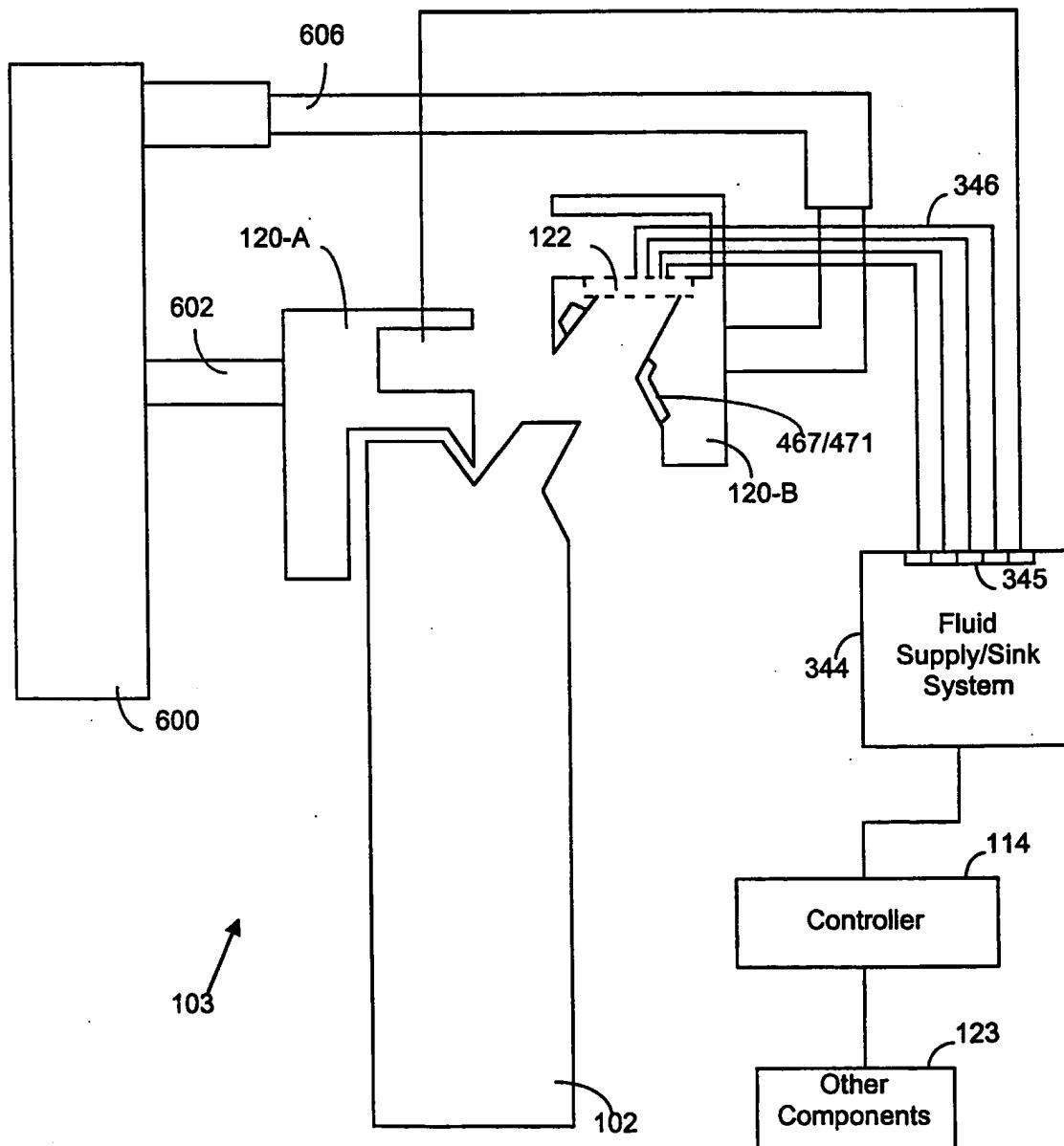


Figure 73

4858

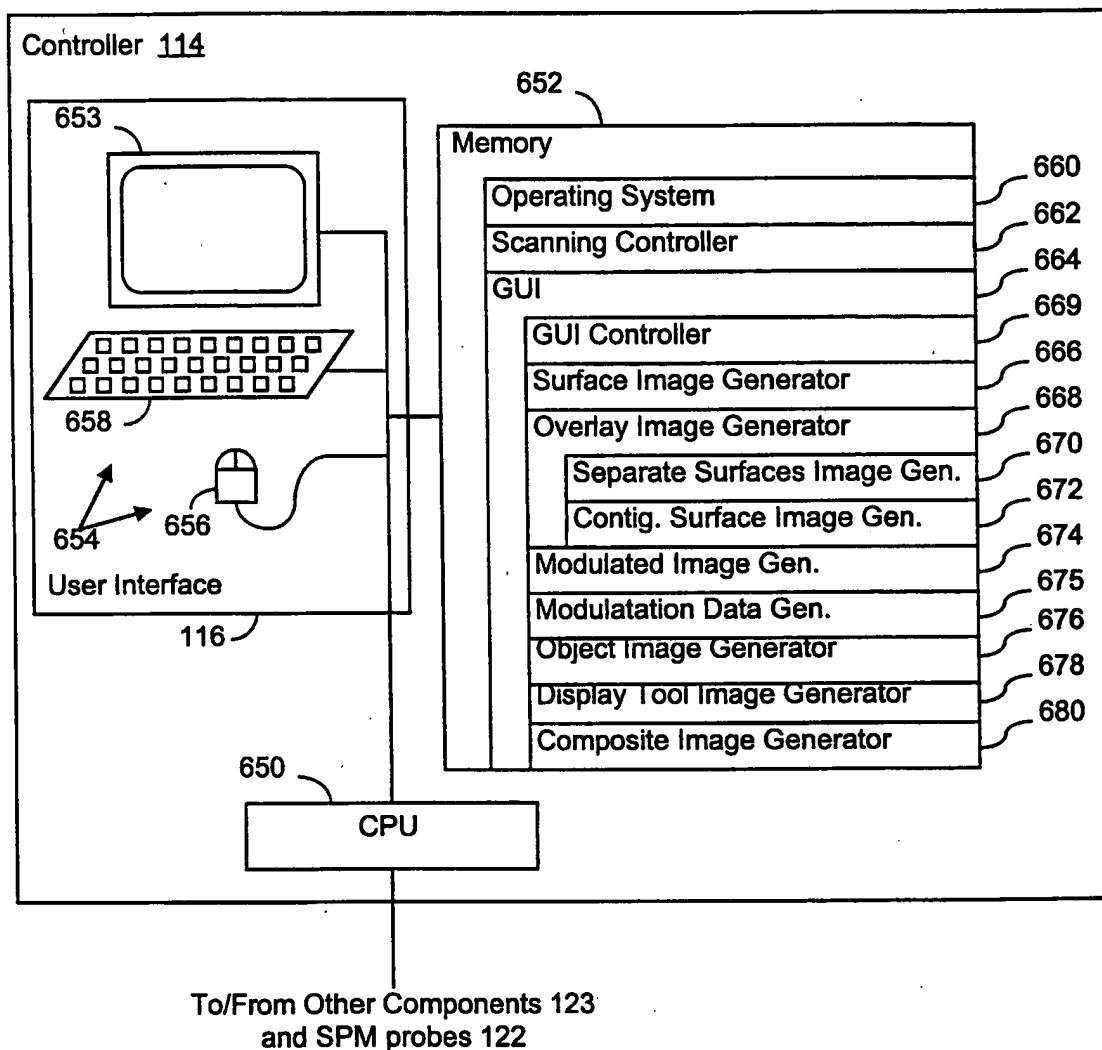
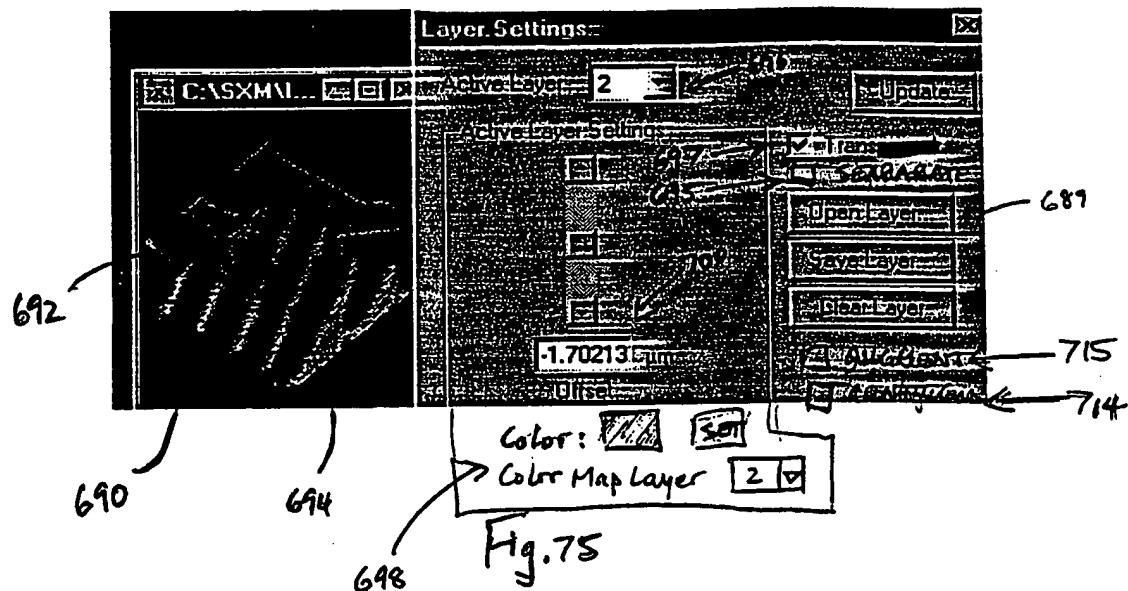
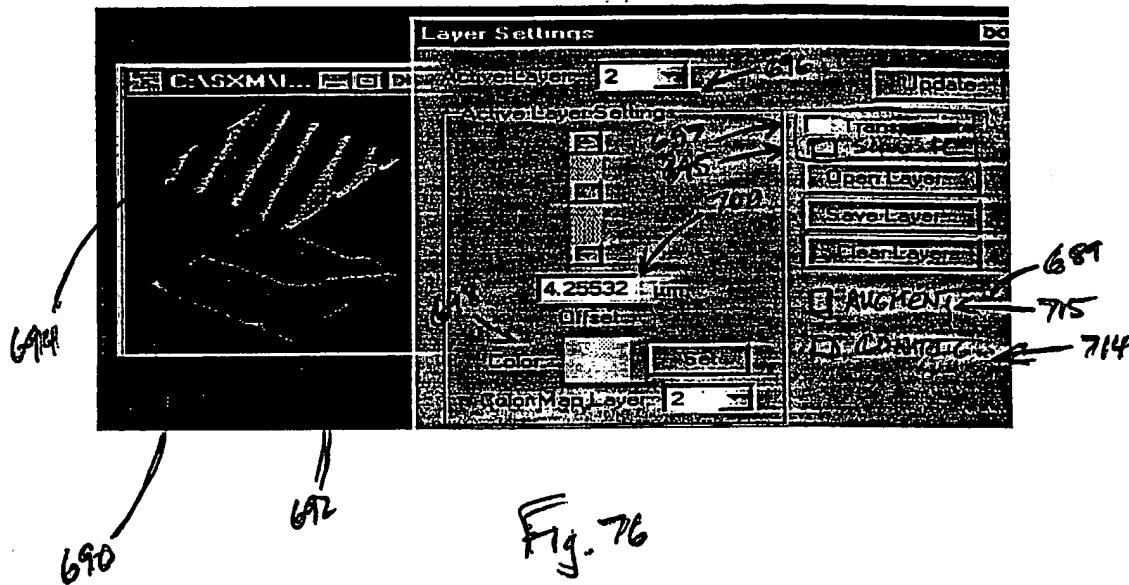


Figure 74

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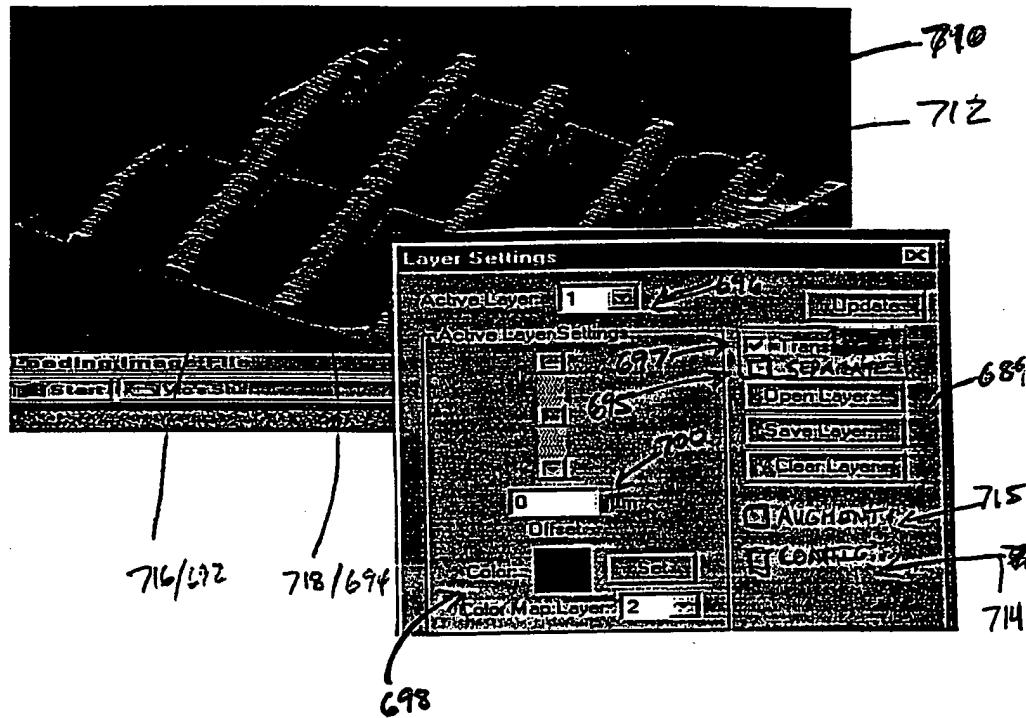


Fig. 77

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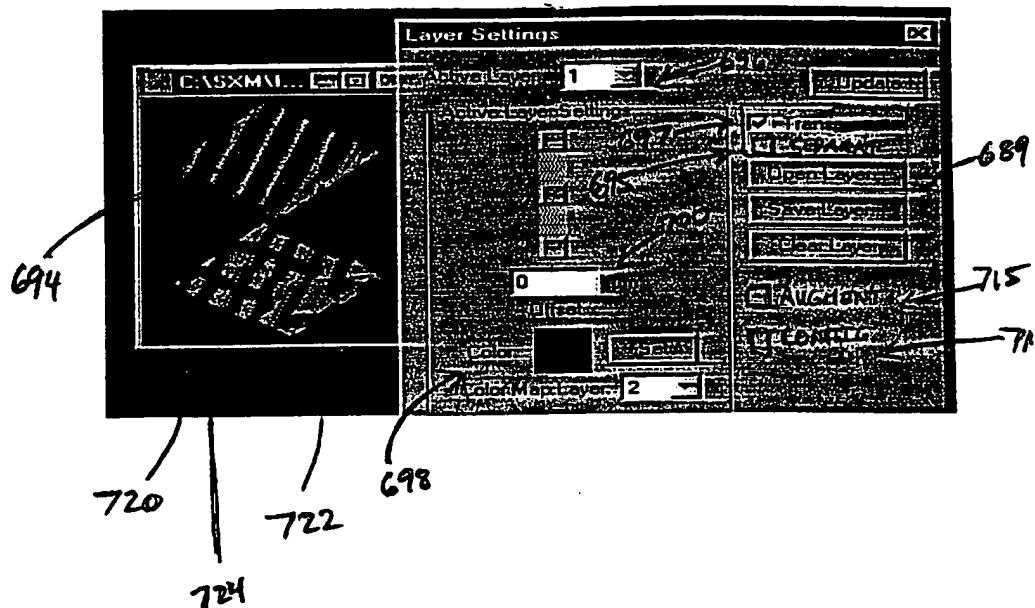


Fig. 78

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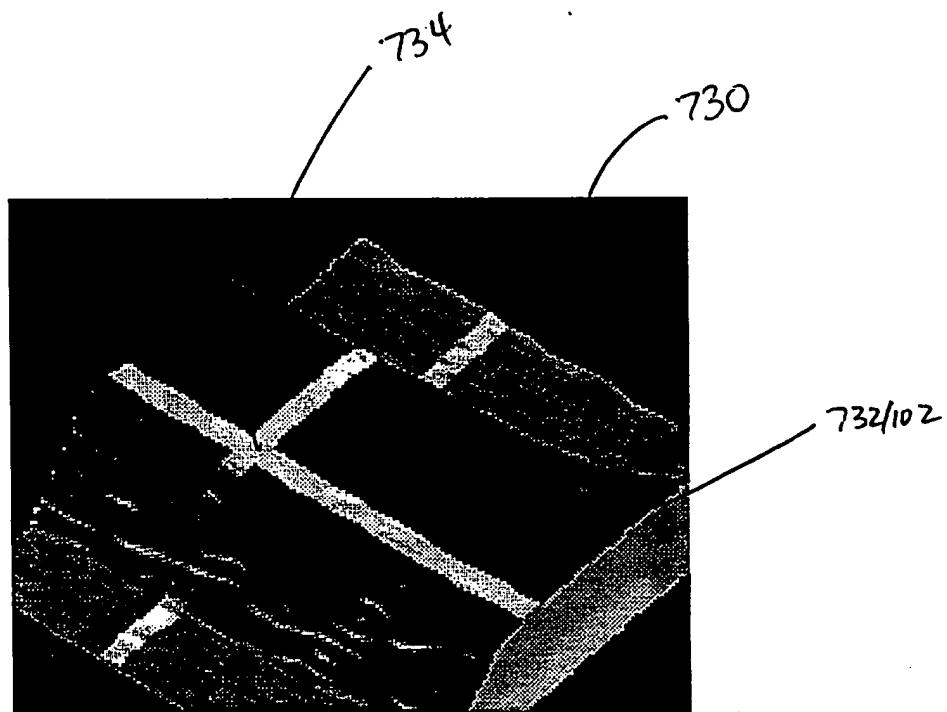


Fig. 79

5458

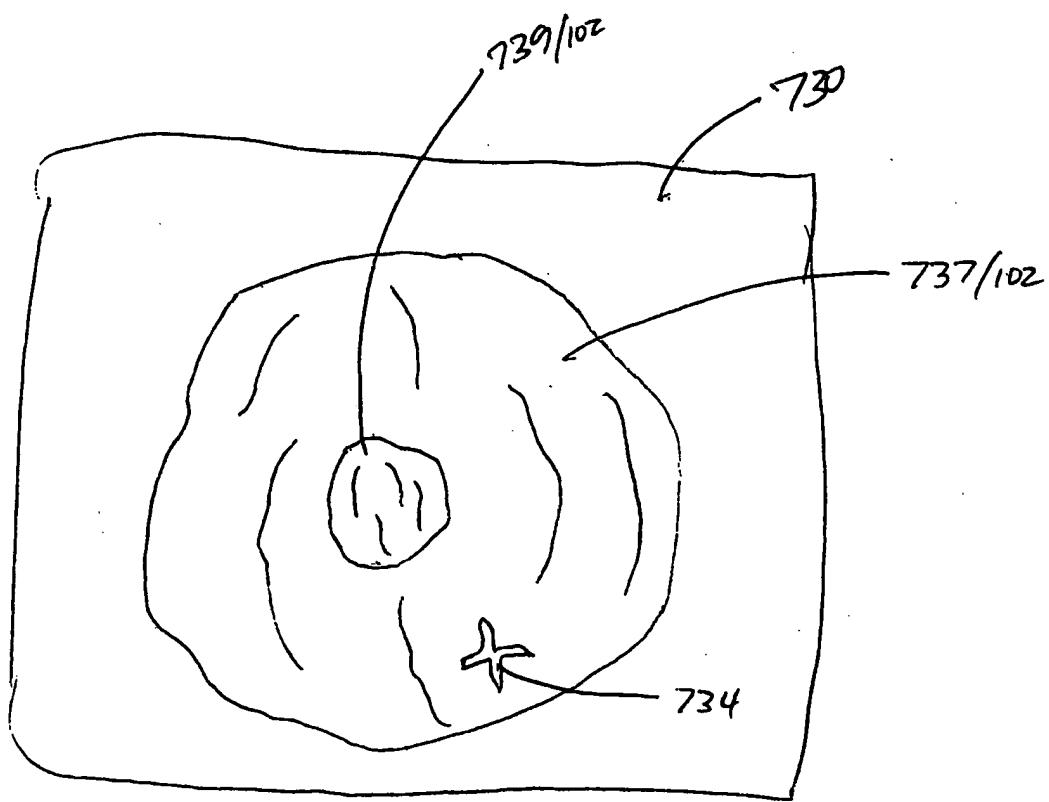


Fig. 80

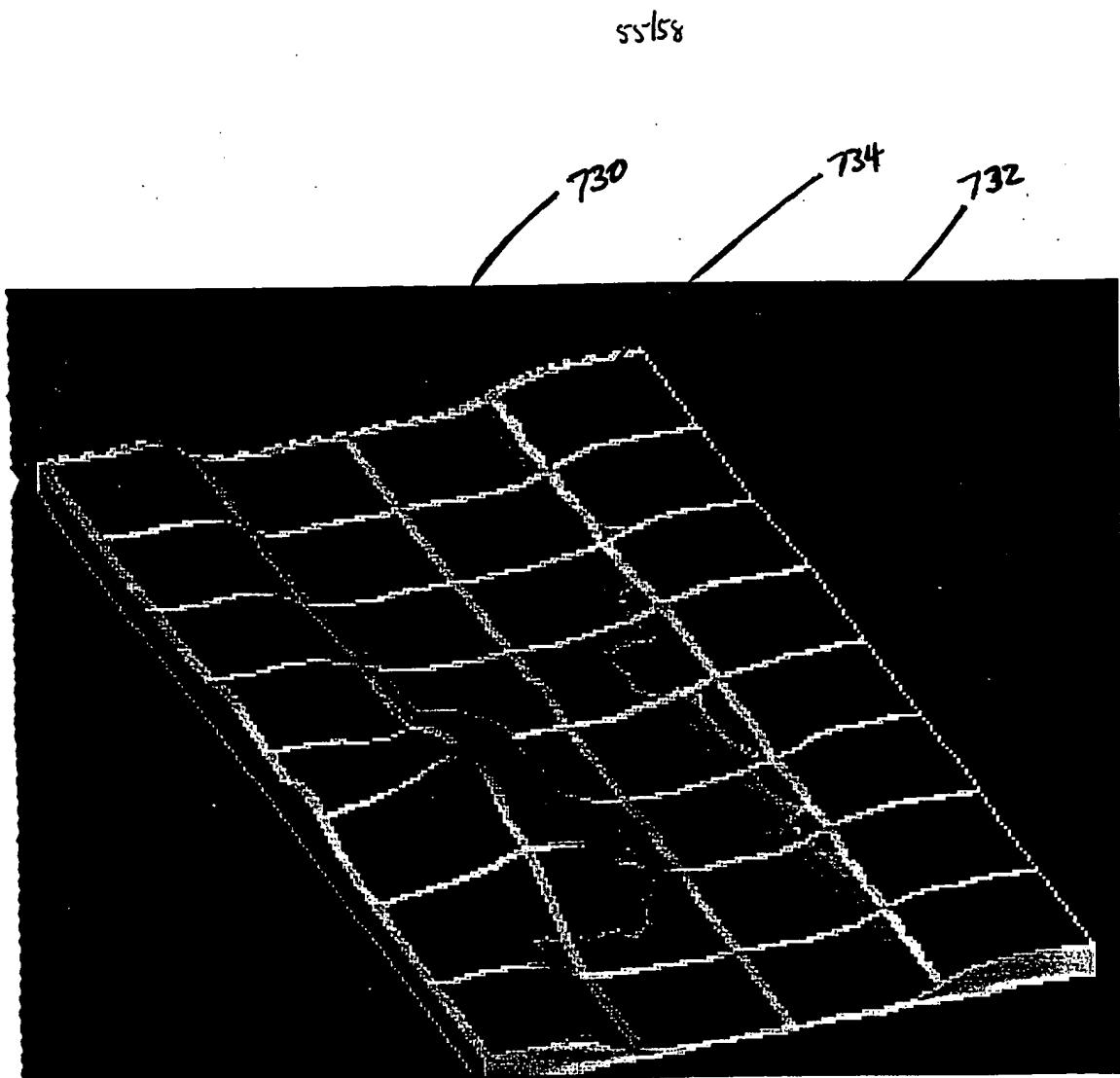
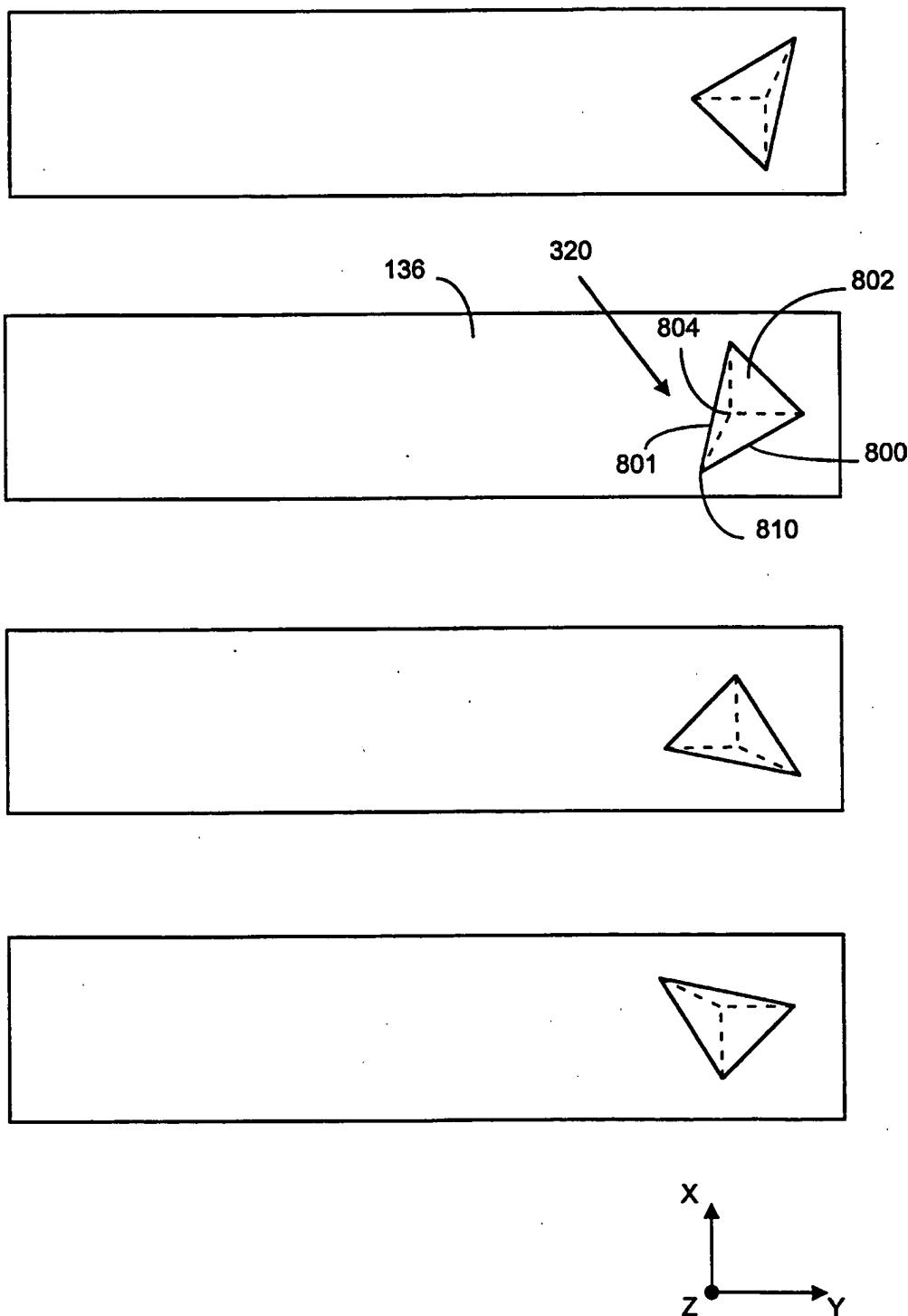


Fig. 81

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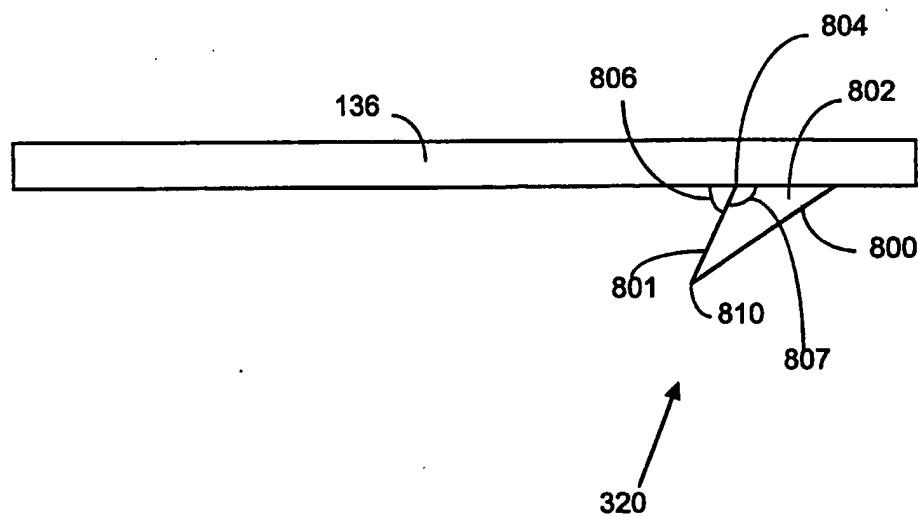


Figure 83

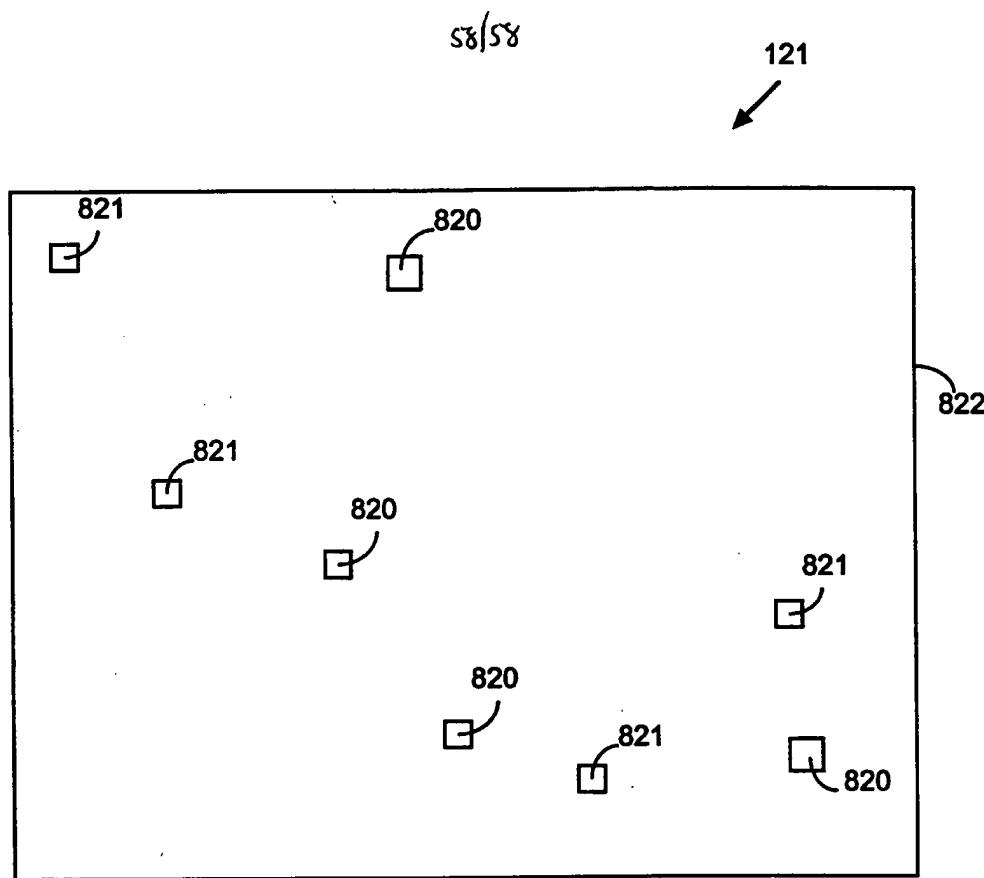


Figure 84

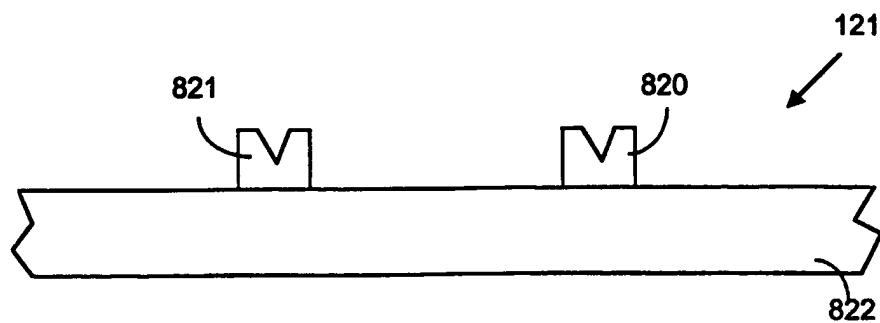


Figure 85

1158

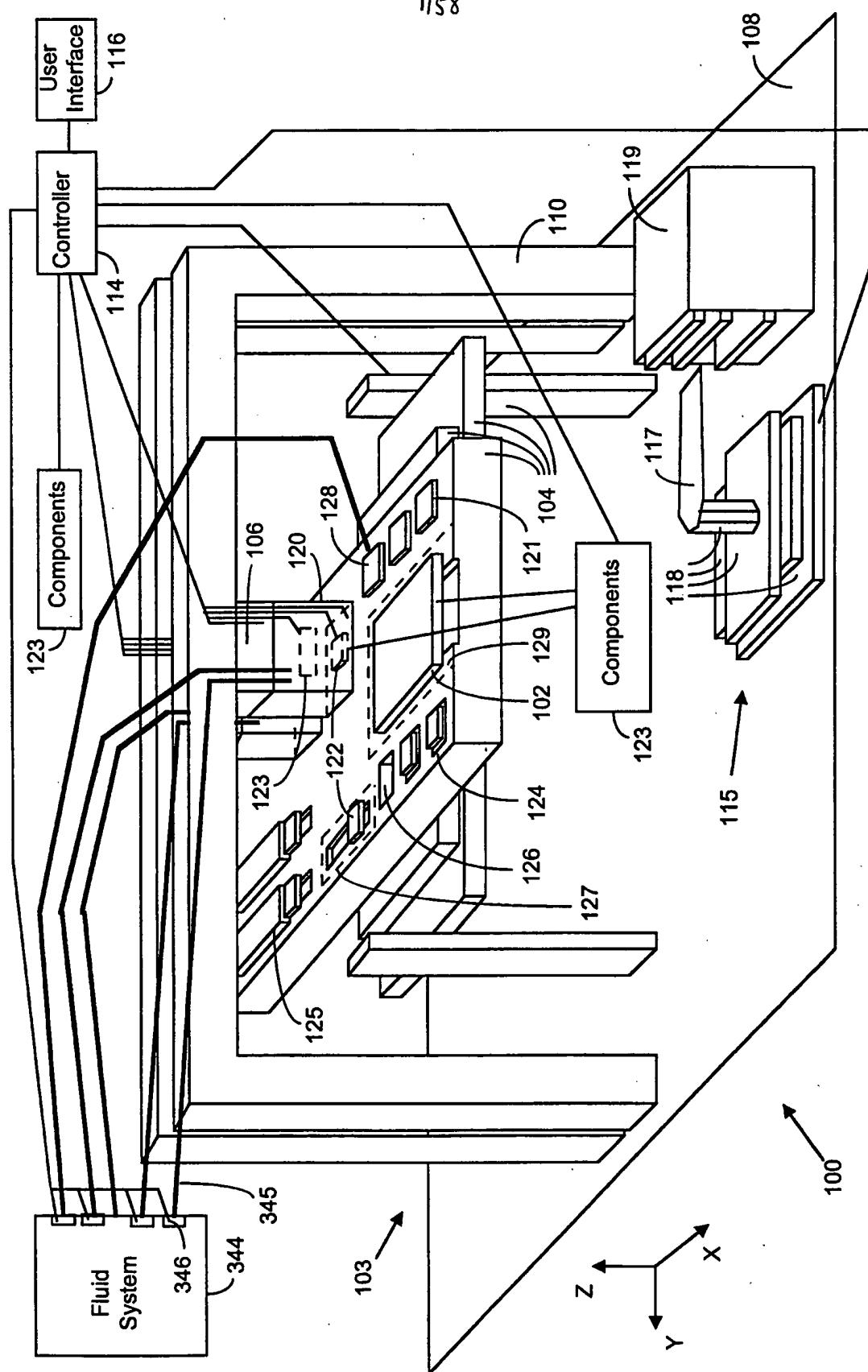


Figure 1

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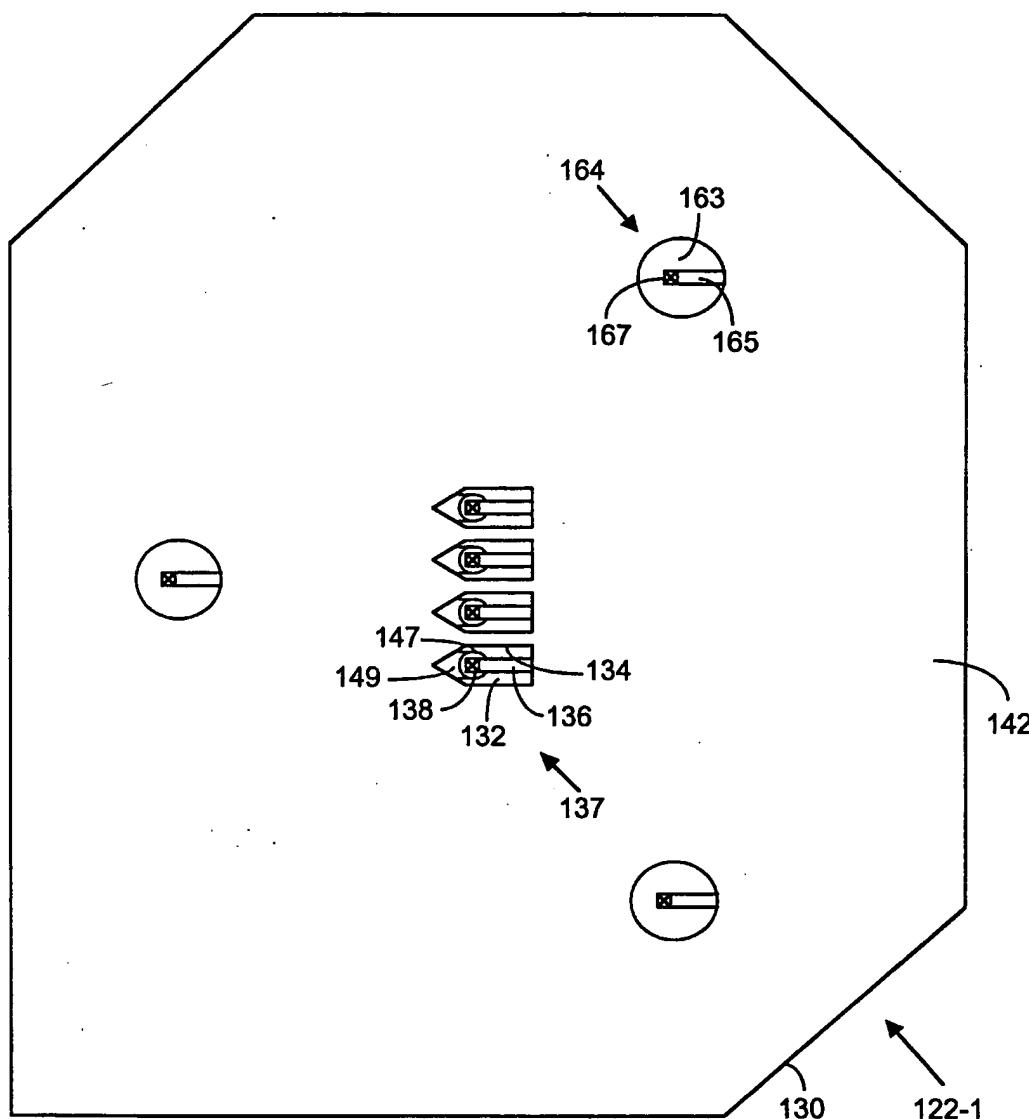


Figure 2

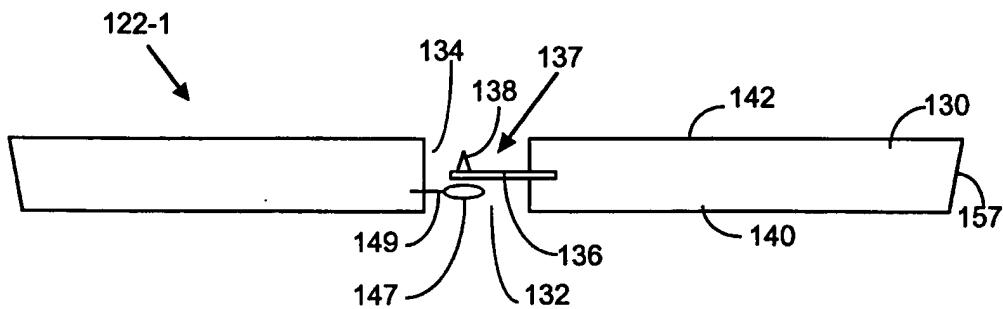


Figure 3

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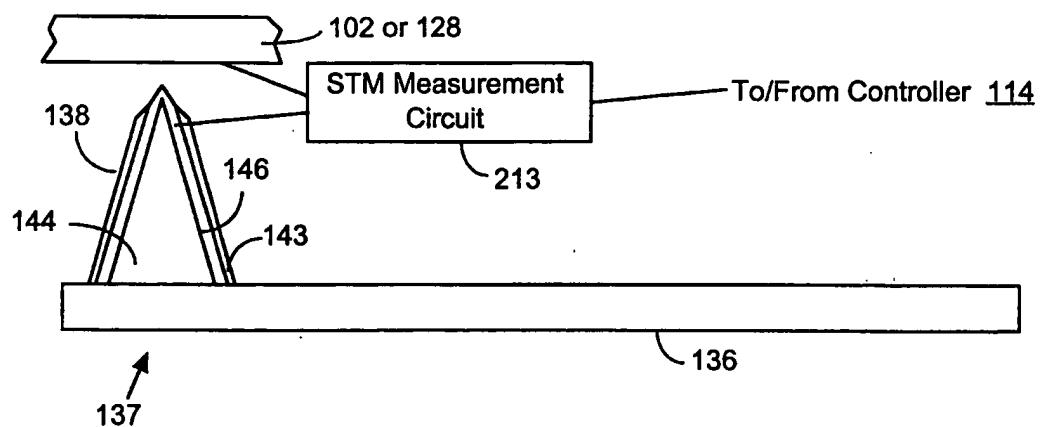


Figure 4

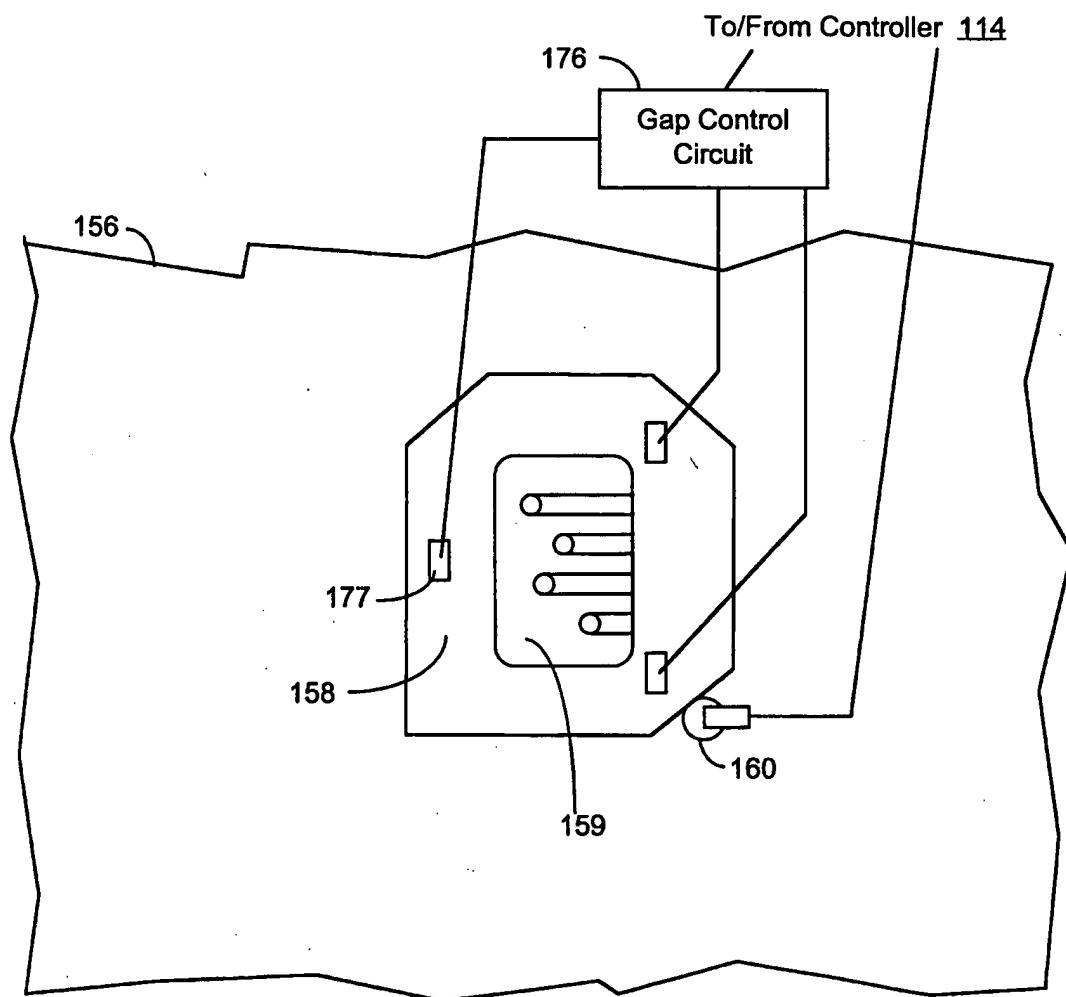


Figure 6

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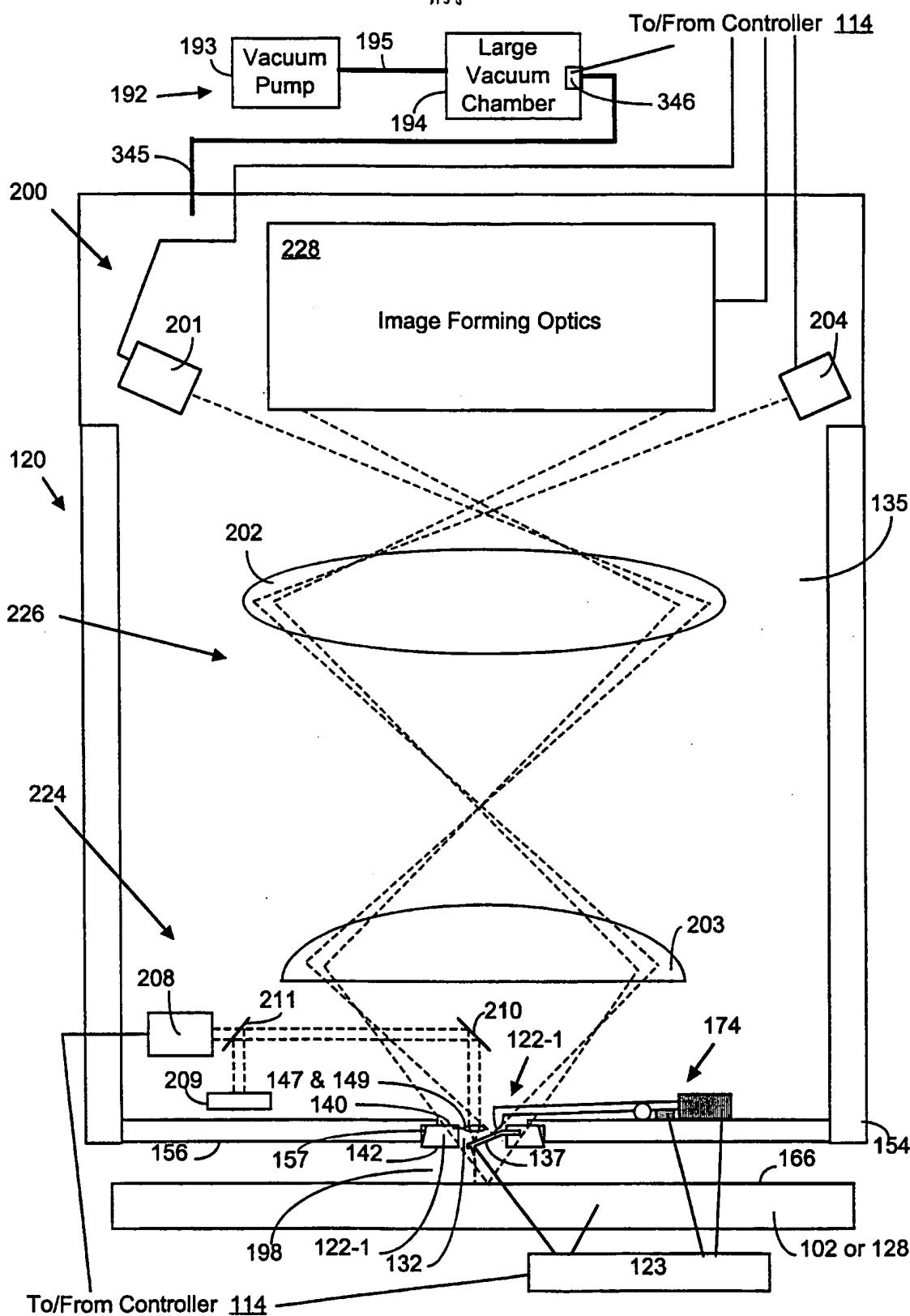


Figure 5

S/S8

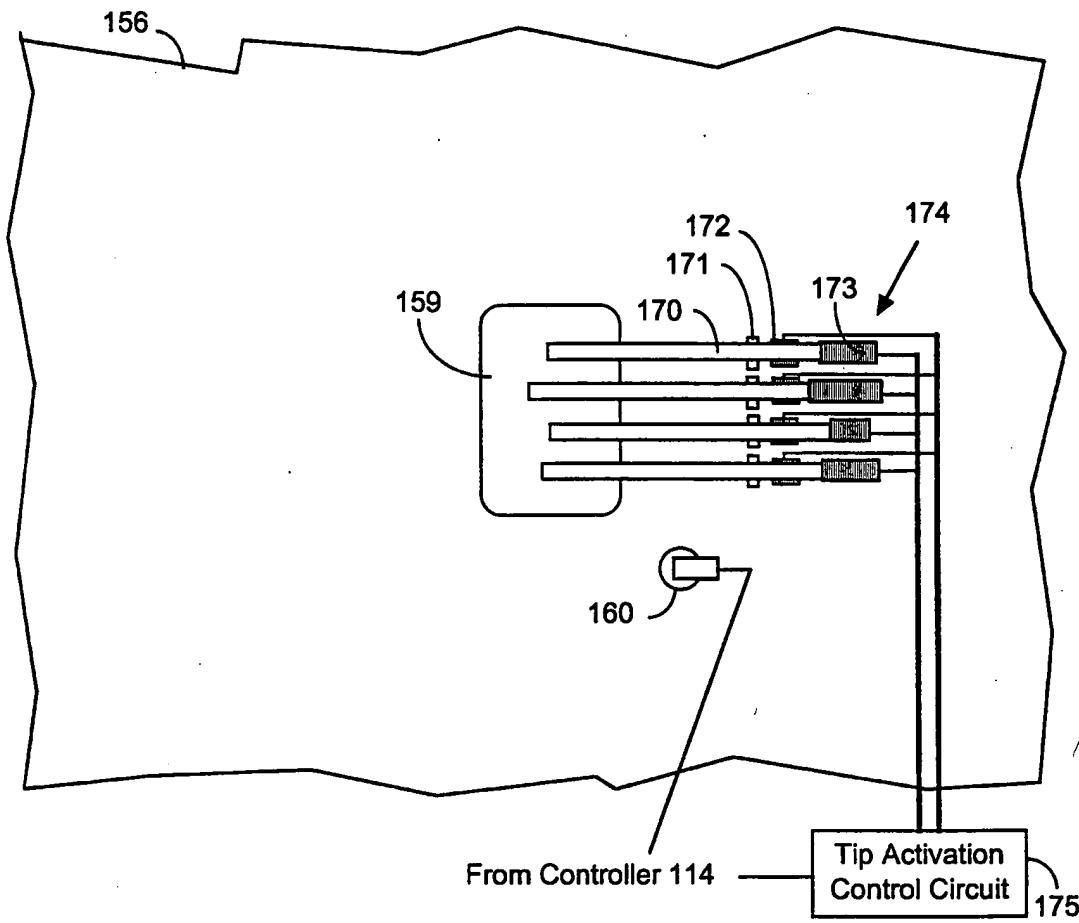


Figure 7

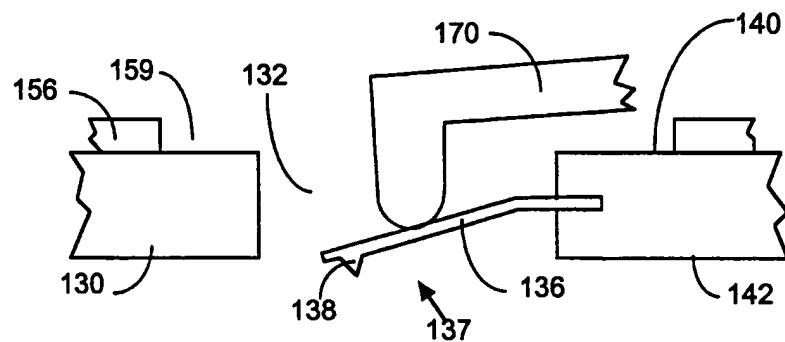


Figure 8

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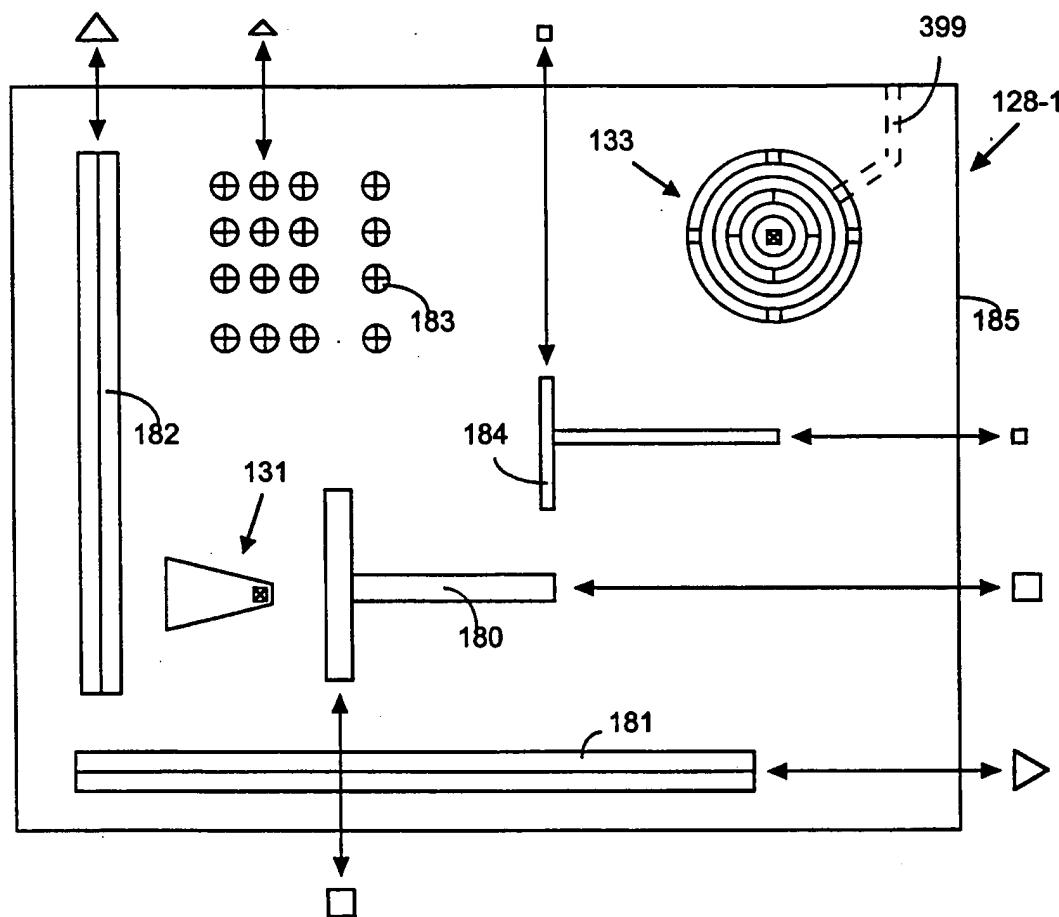


Figure 9

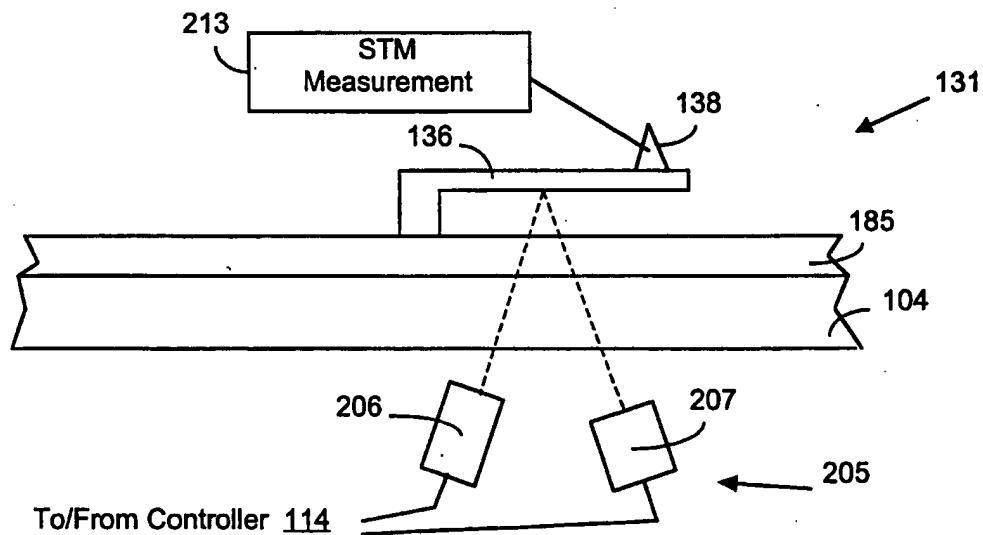


Figure 10

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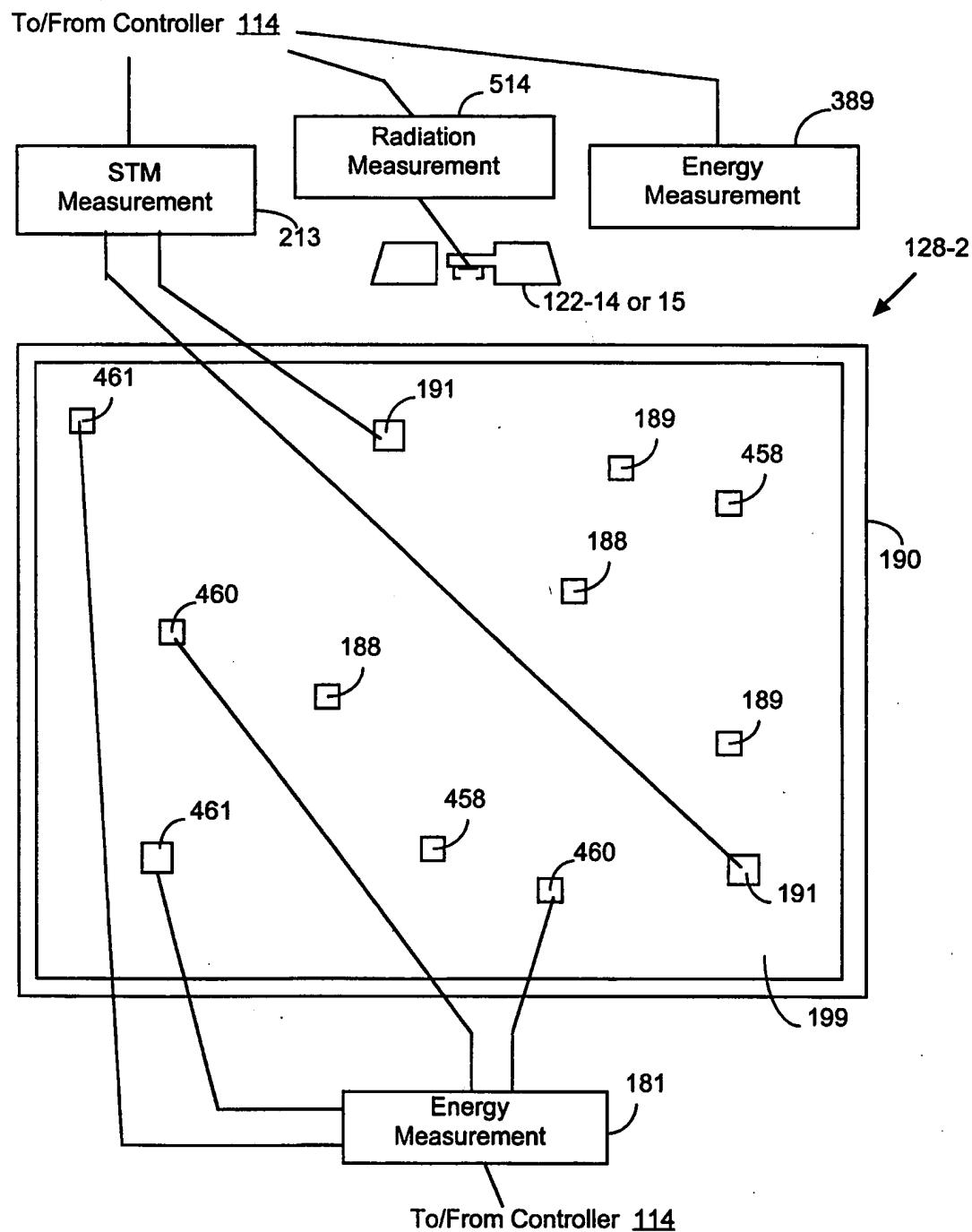


Figure 11

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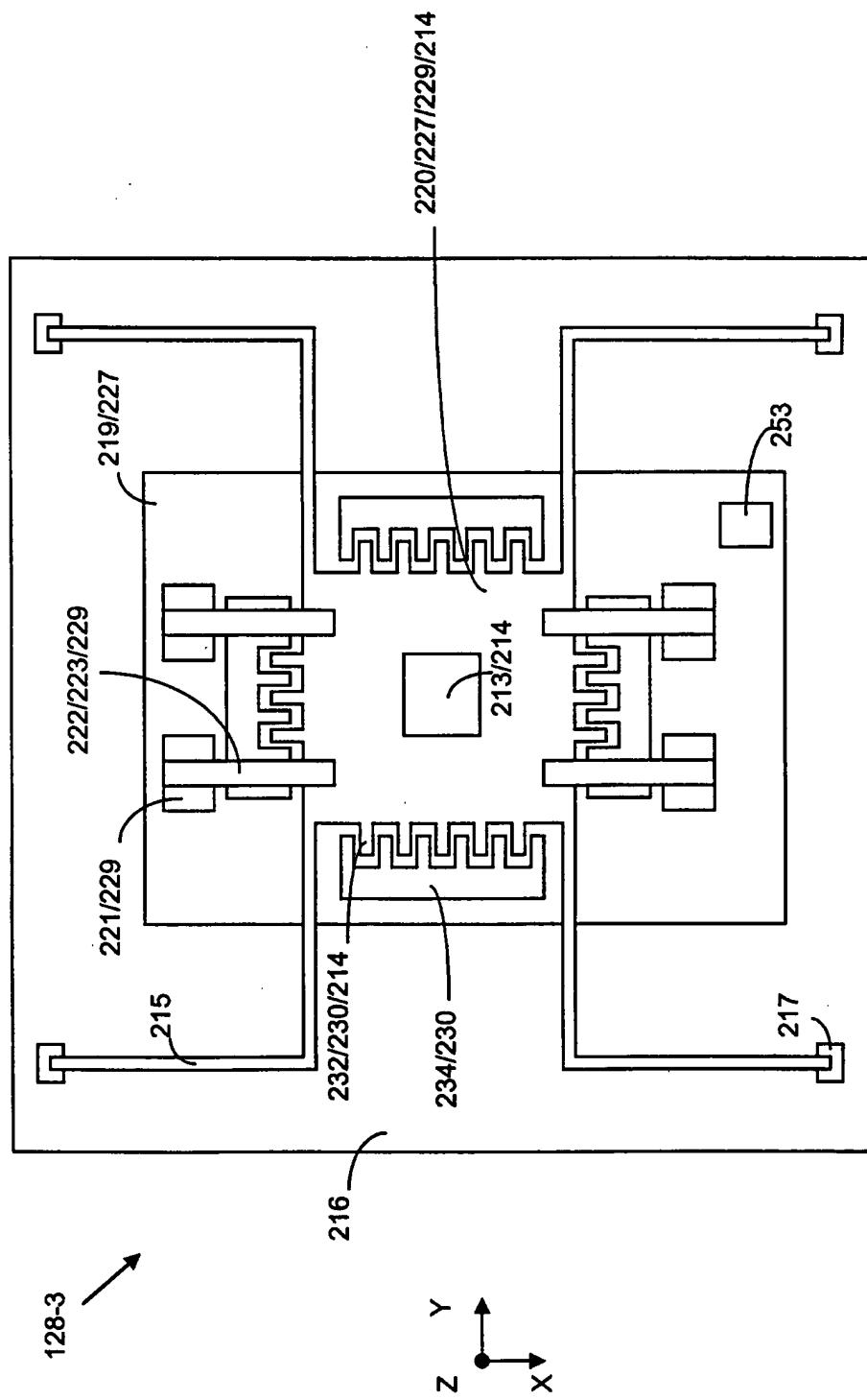


Figure 12

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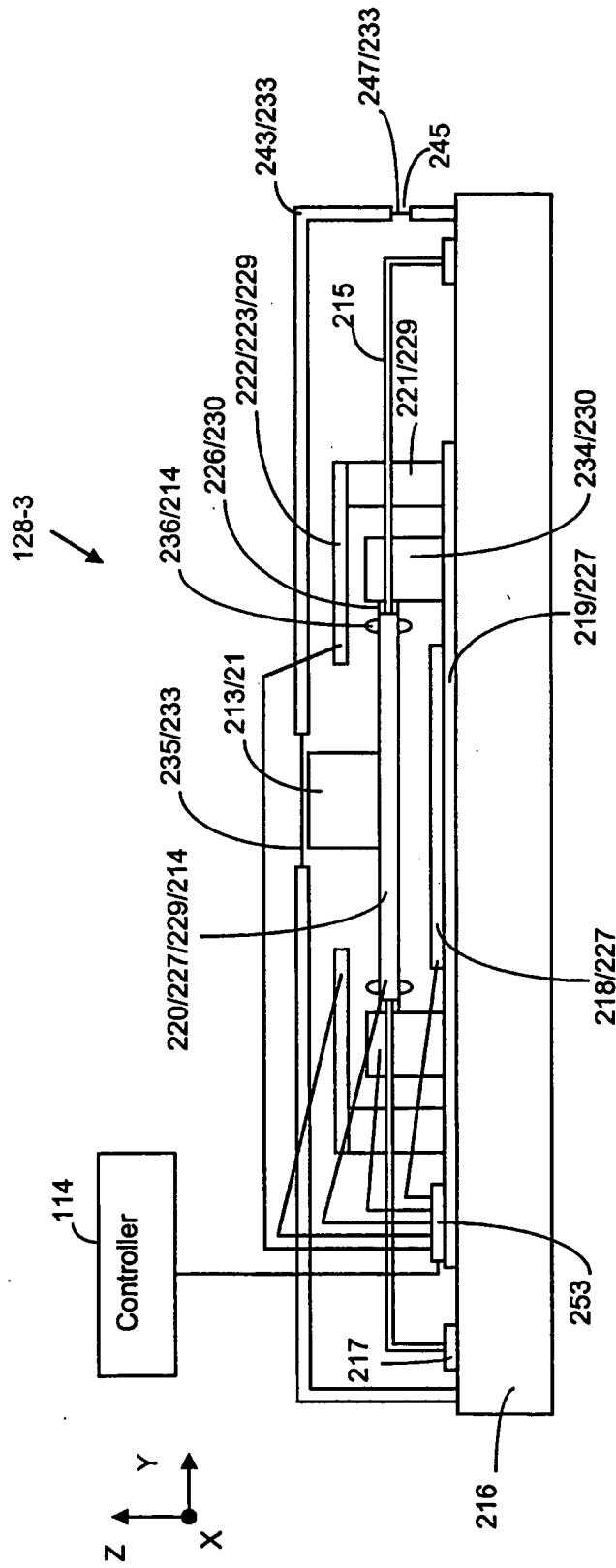


Figure 13

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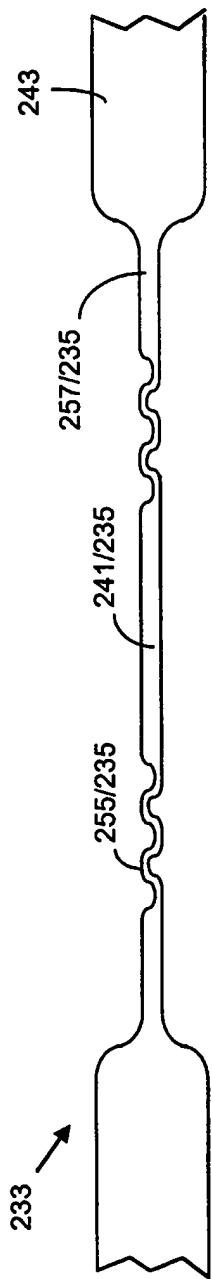


Figure 14

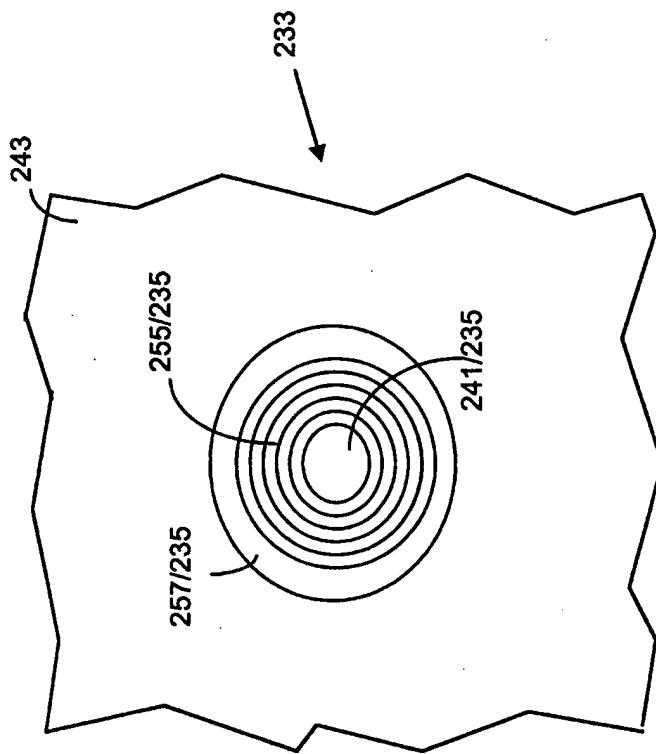


Figure 15

1158

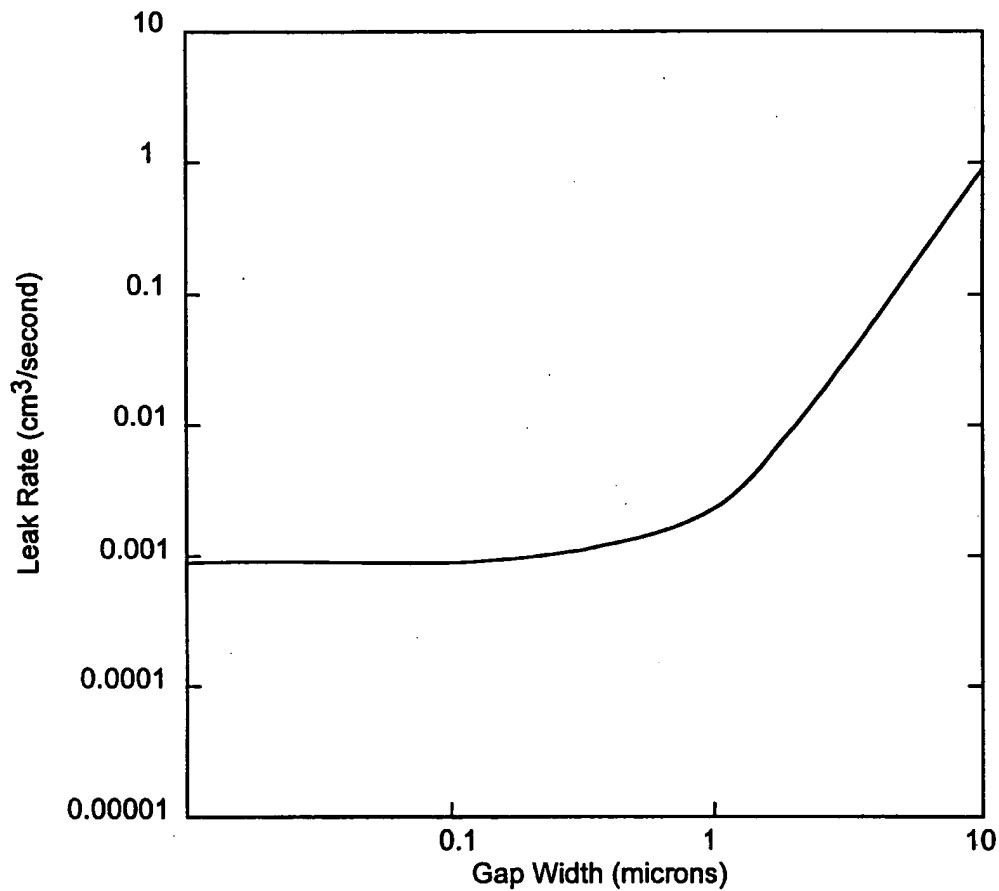


Figure 16

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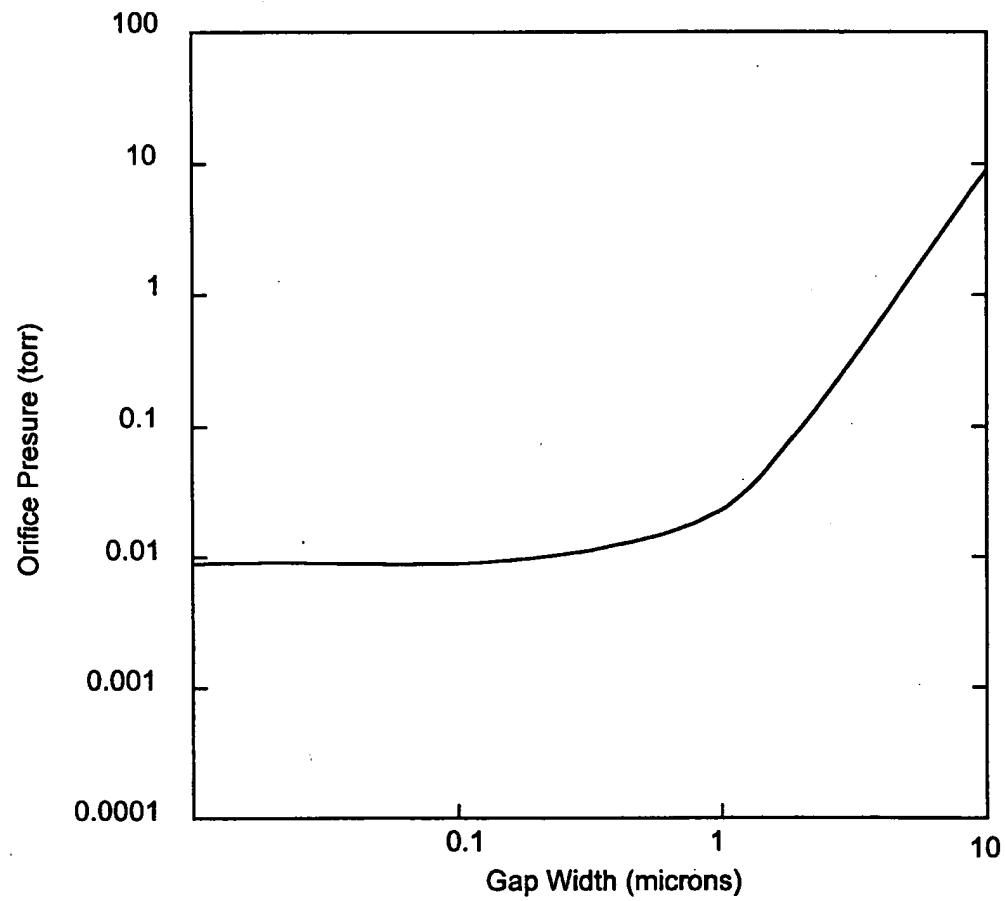


Figure 17

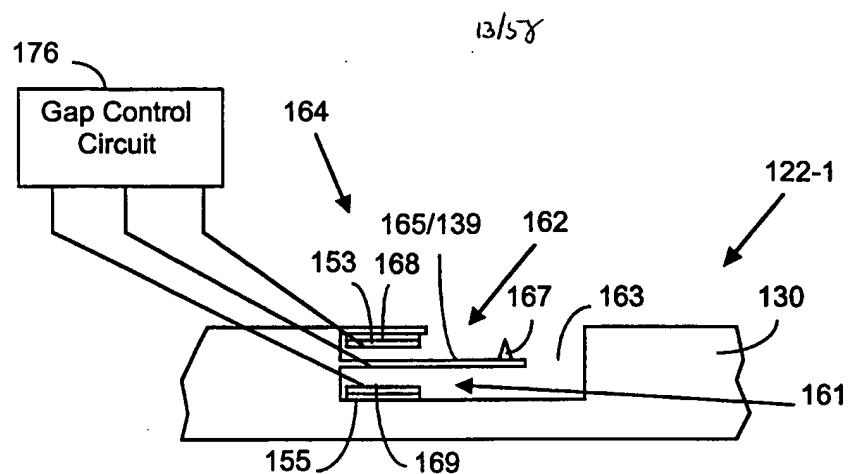


Figure 18

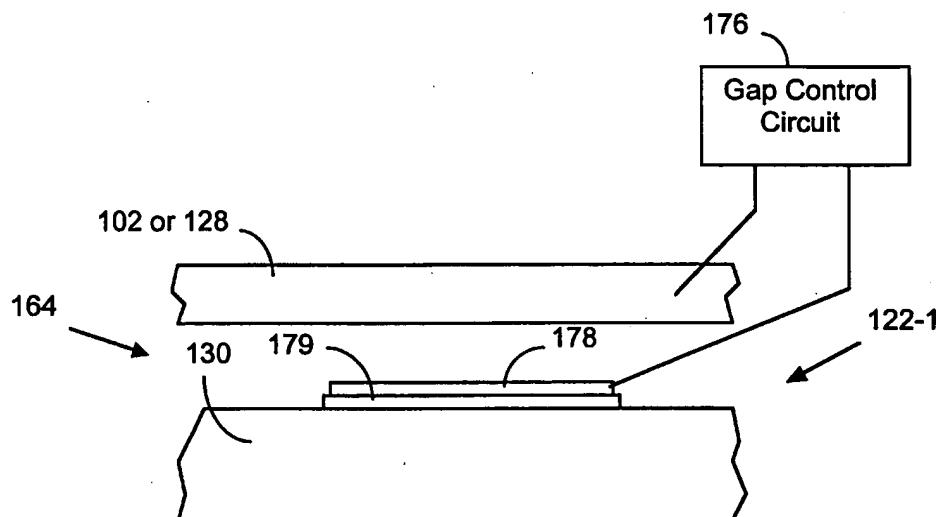


Figure 19

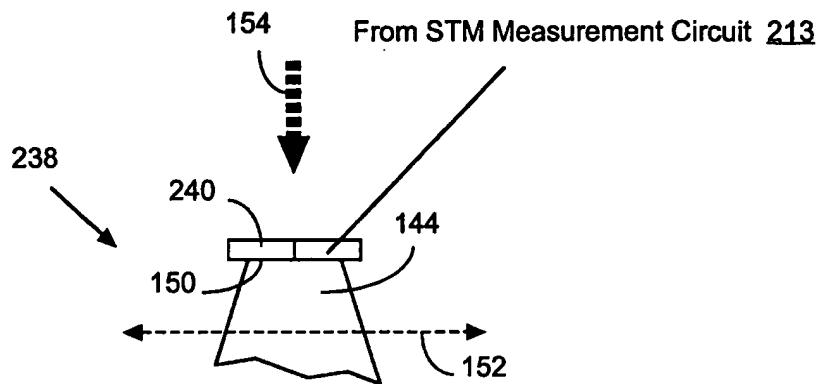


Figure 21

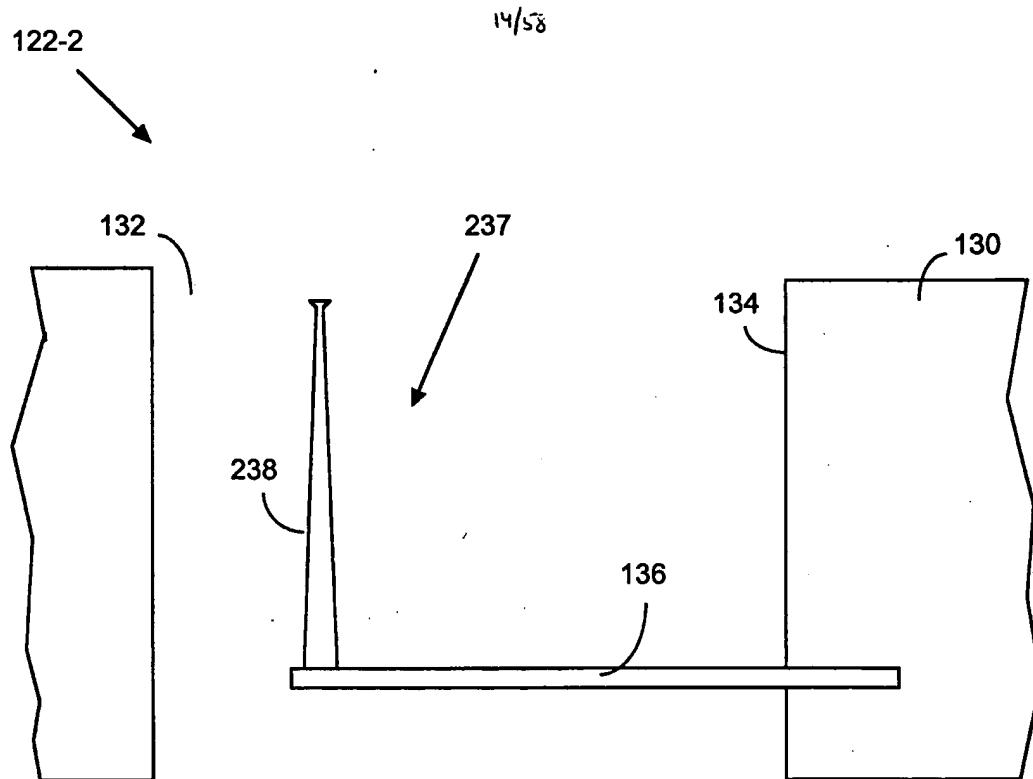


Figure 20

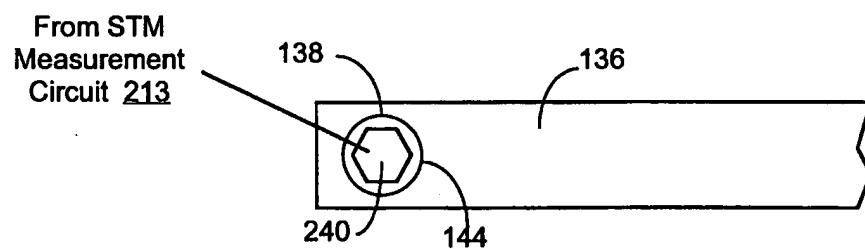


Figure 22

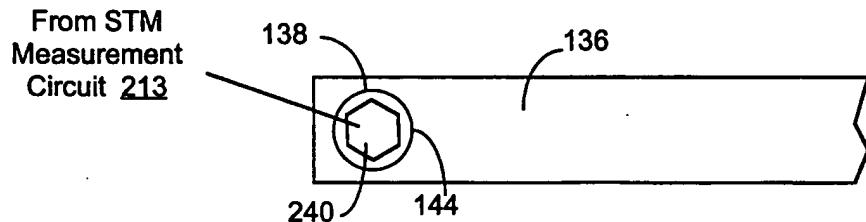


Figure 23

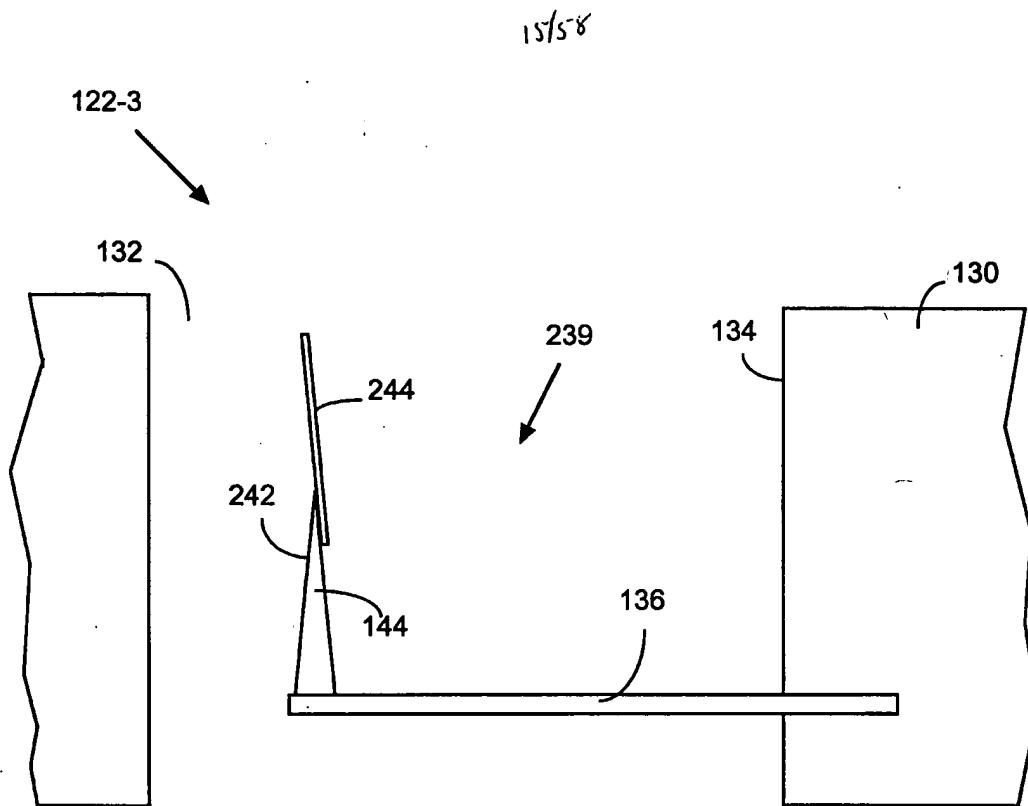


Figure 24

From STM Measurement Circuit 213

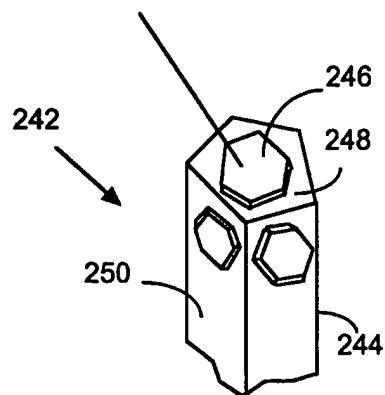


Figure 25

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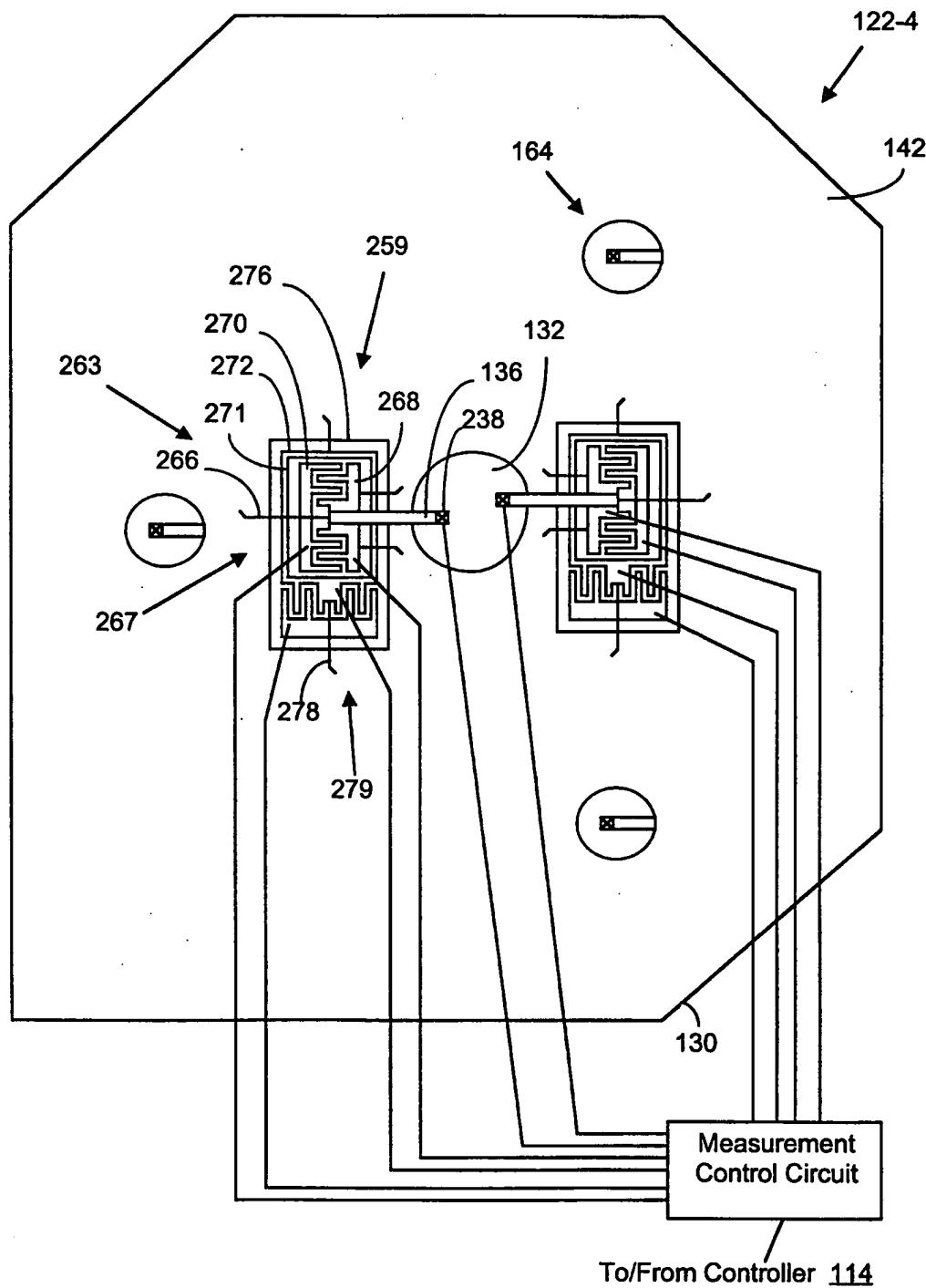


Figure 26

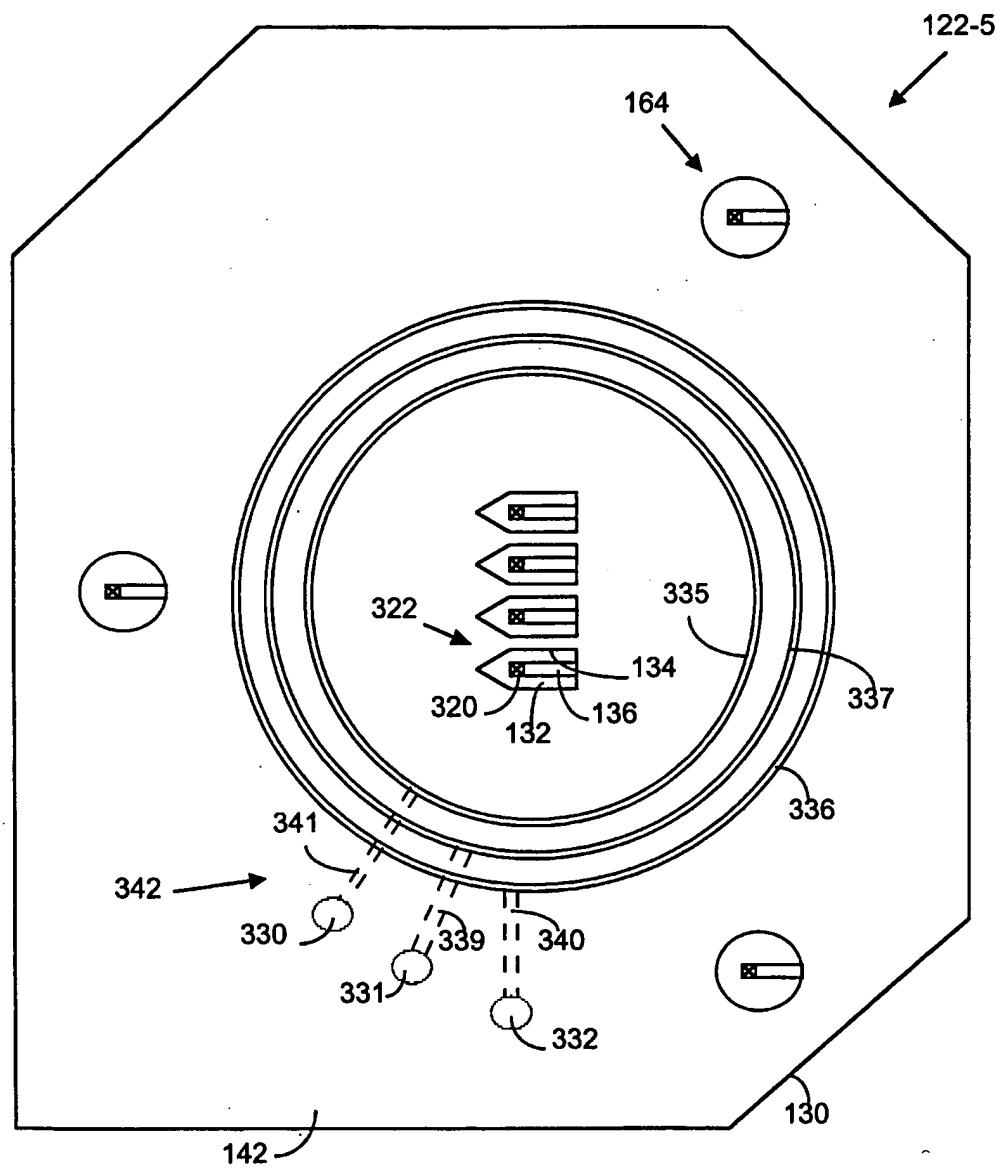


Figure 27

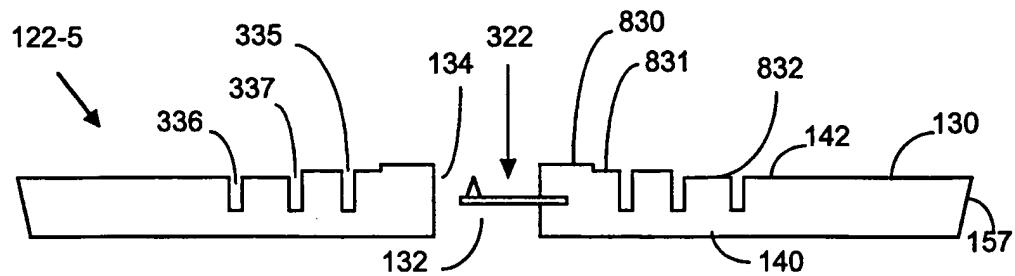


Figure 86

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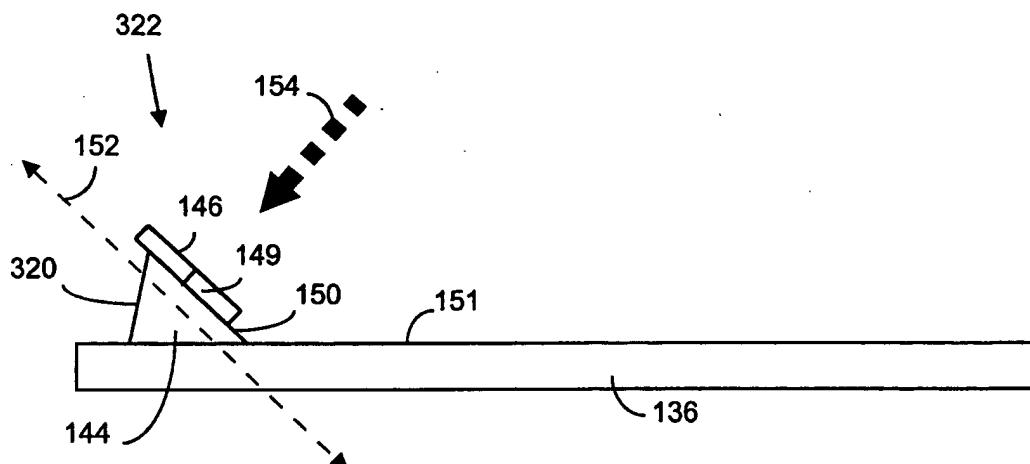


Figure 28

136 151 144 320 146 149 150 152 322 154

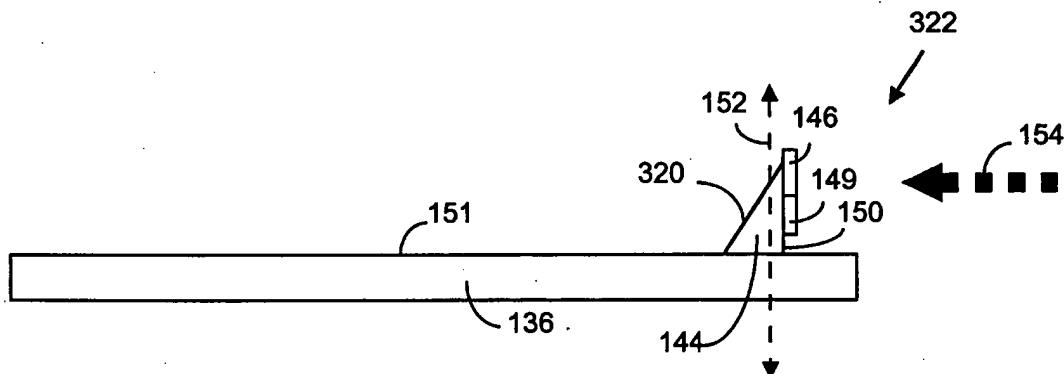


Figure 29

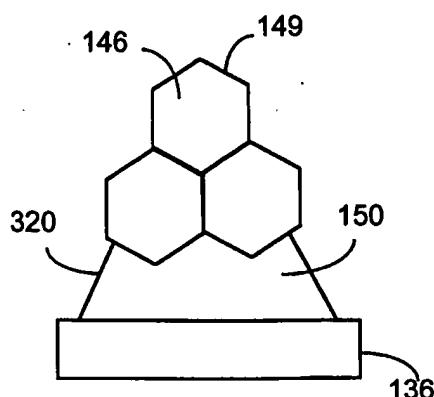


Figure 30

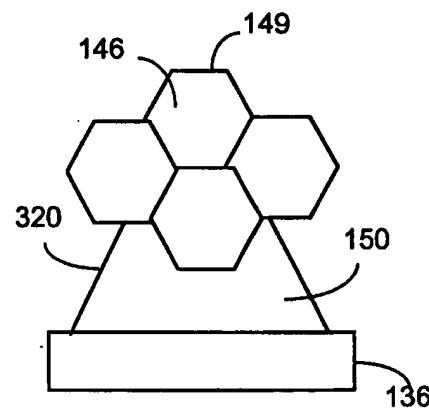


Figure 31

1958

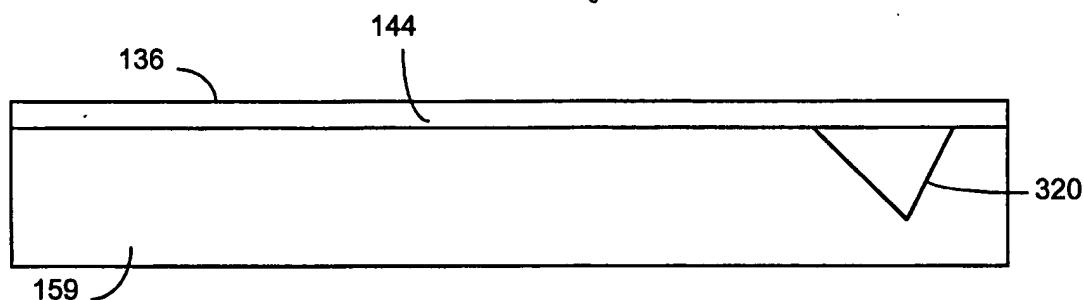


Figure 32

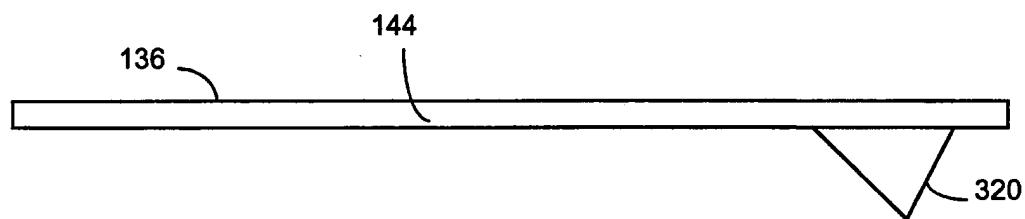


Figure 33

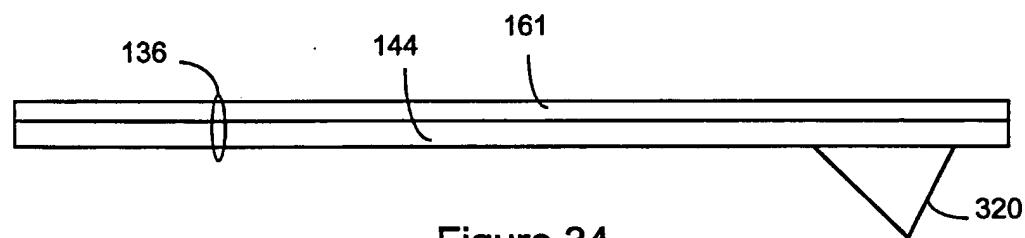


Figure 34

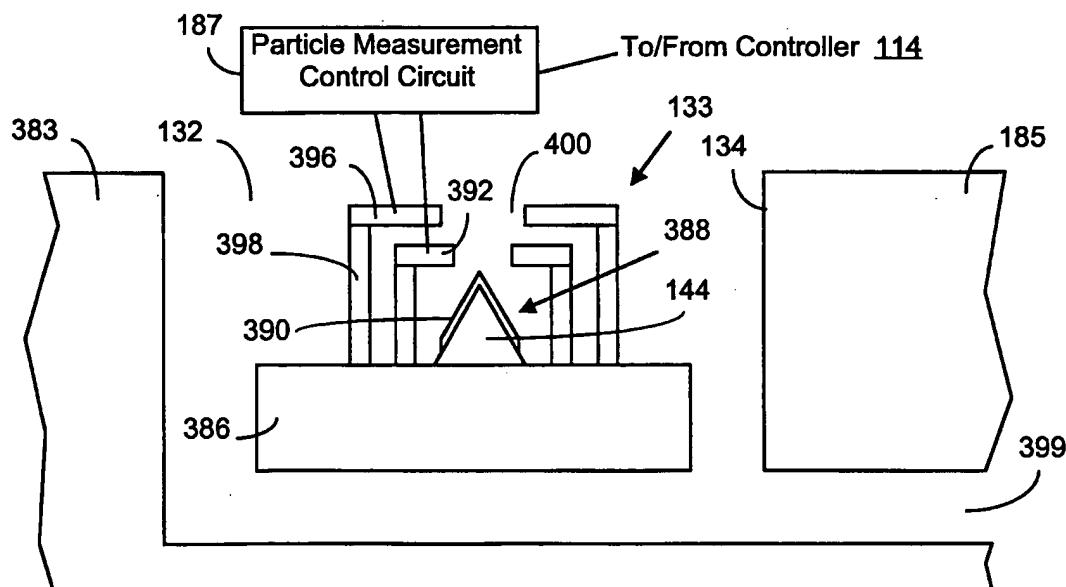


Figure 87

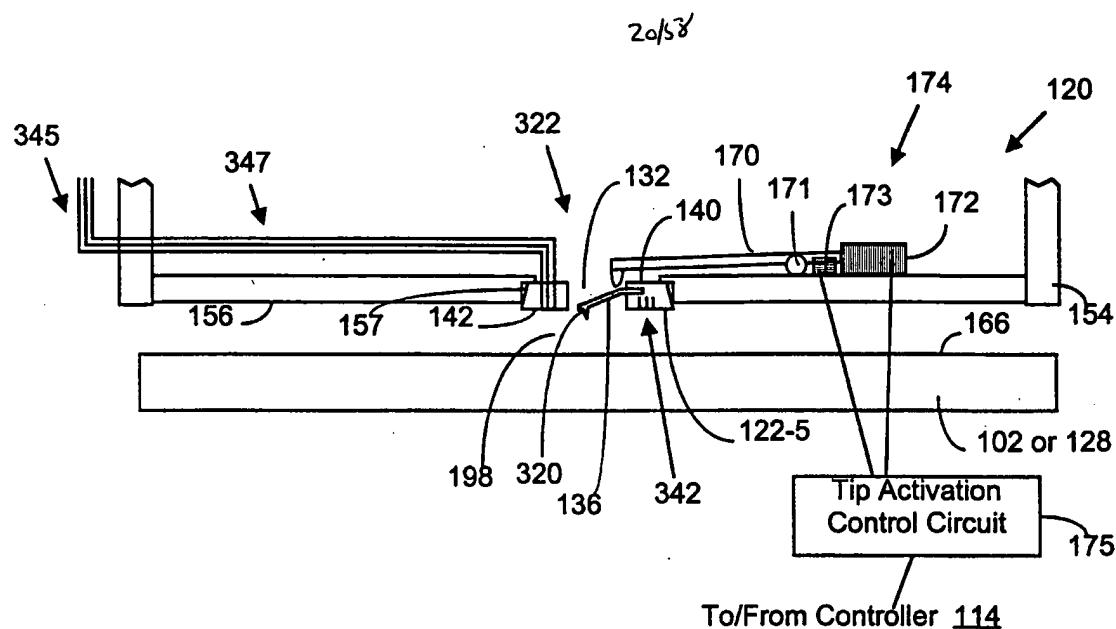


Figure 35

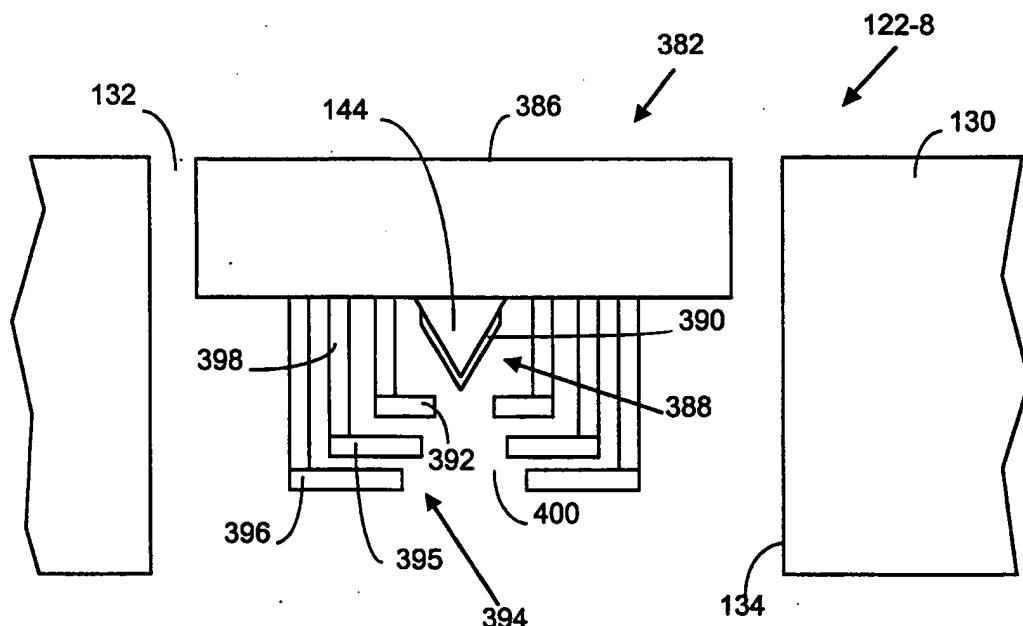


Figure 41

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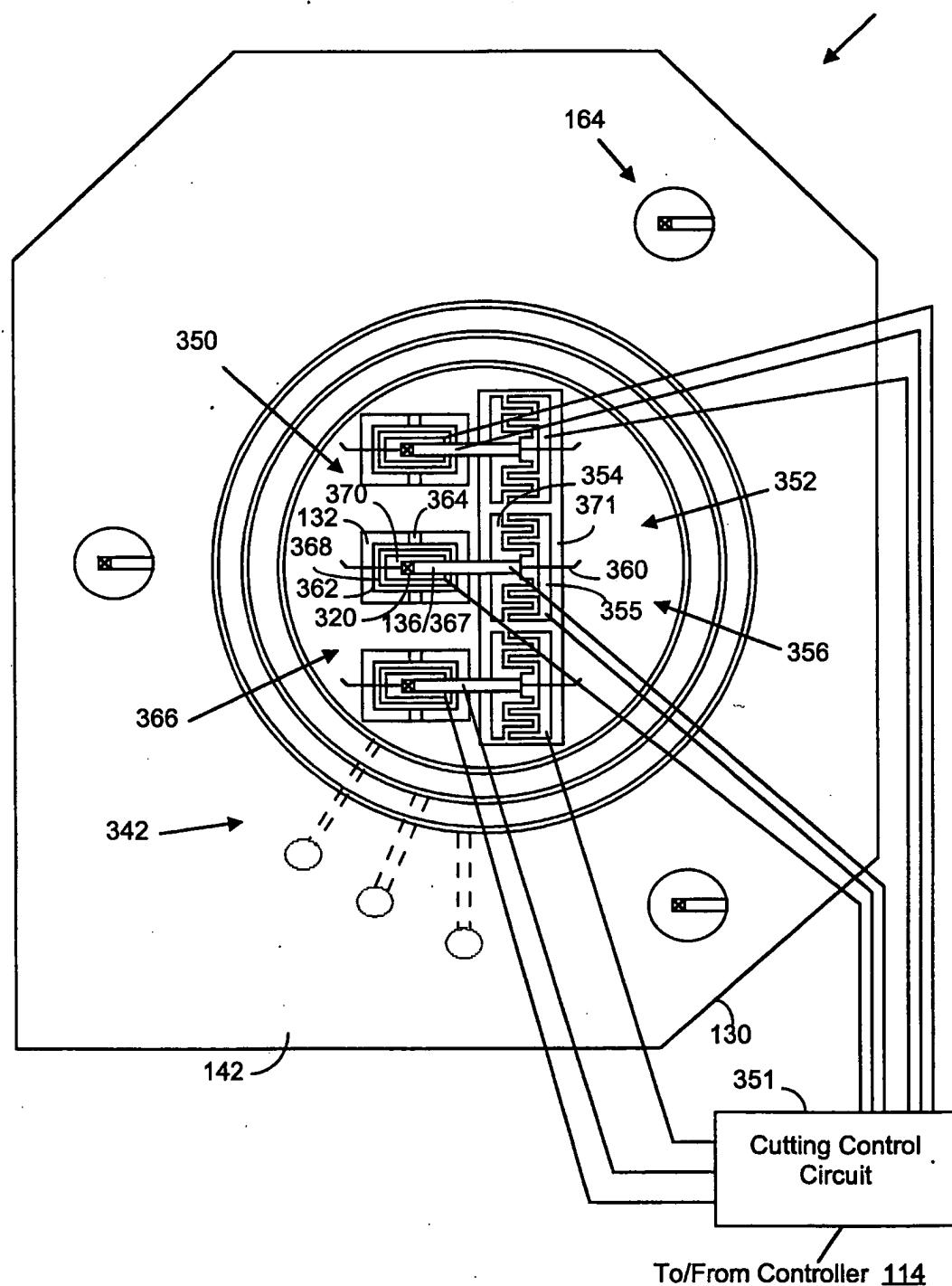


Figure 36

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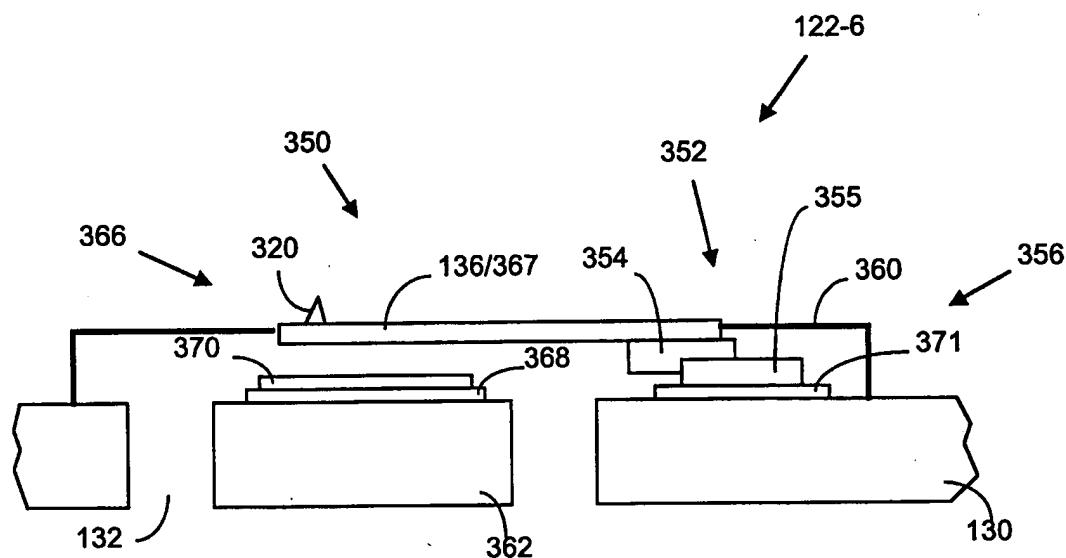


Figure 37

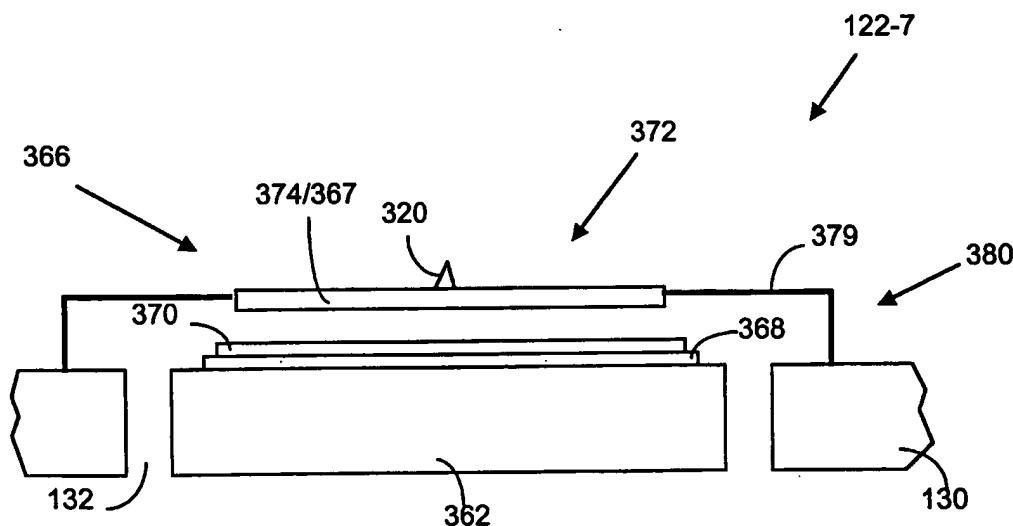


Figure 39

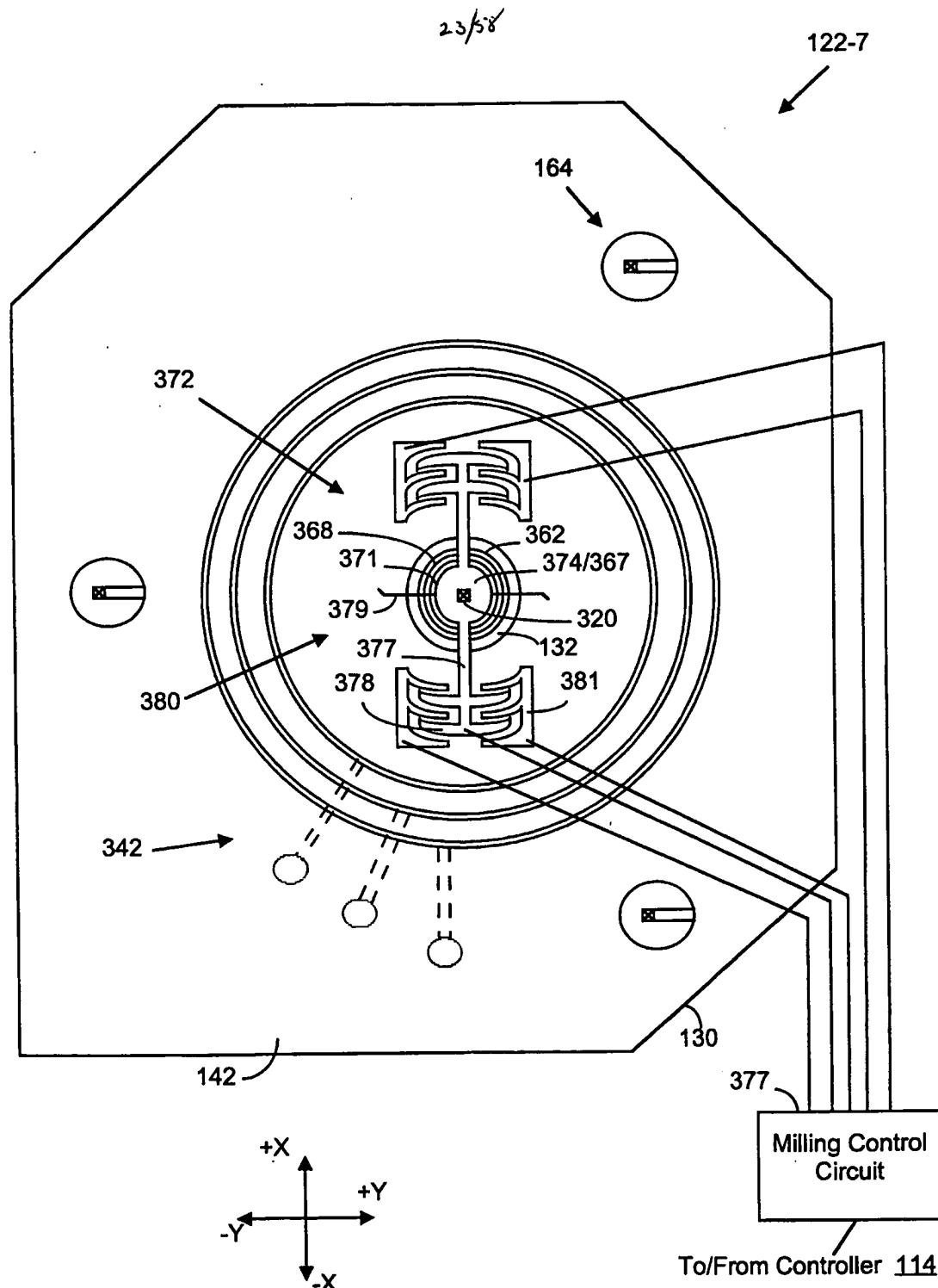


Figure 38

SUBSTITUTE SHEET (RULE 26)

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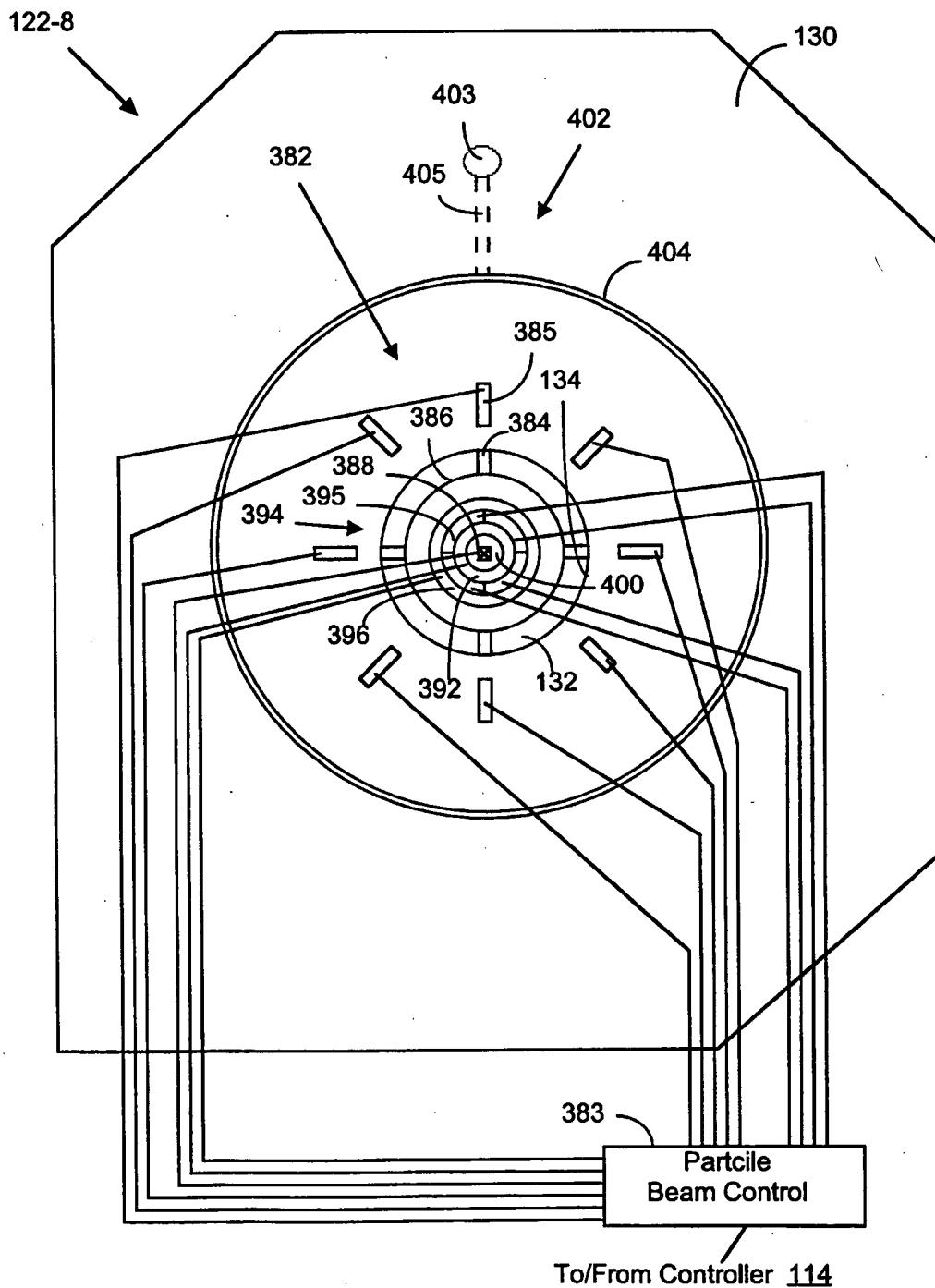


Figure 40

SUBSTITUTE SHEET (RULE 26)

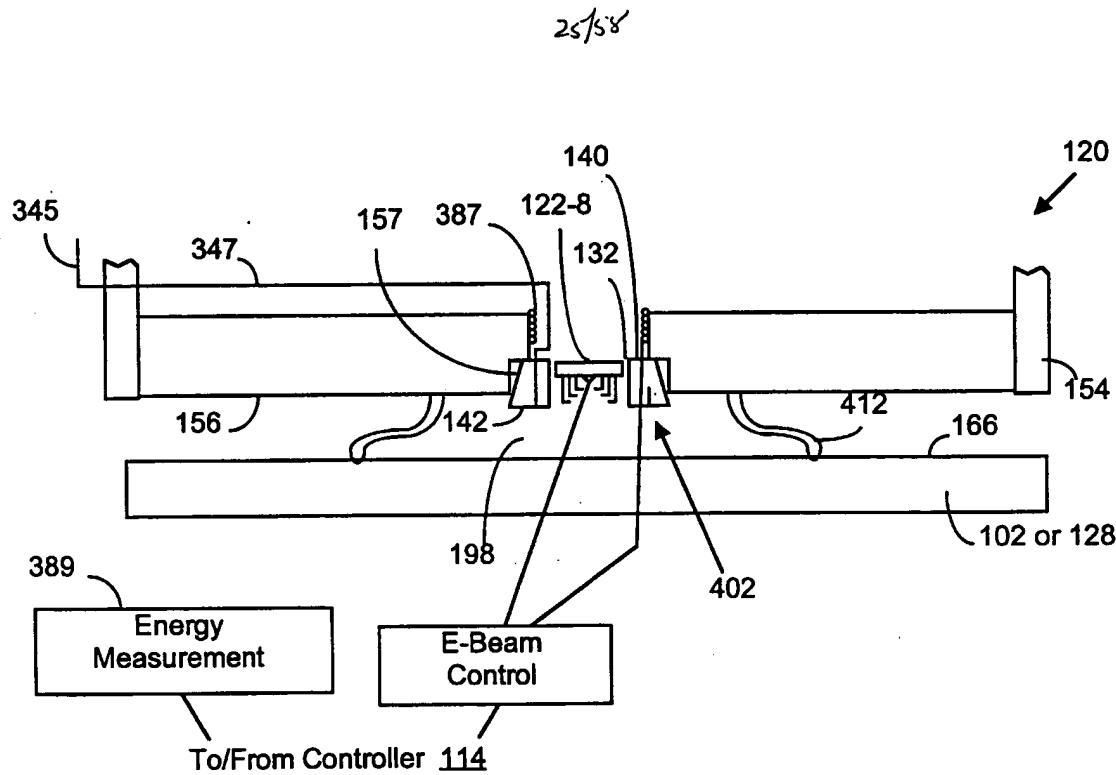


Figure 42

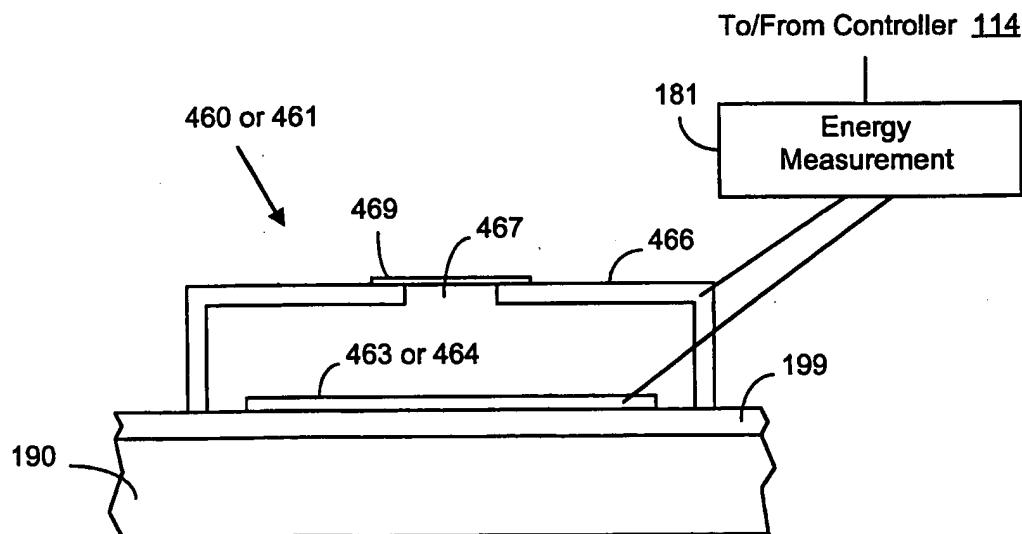


Figure 52

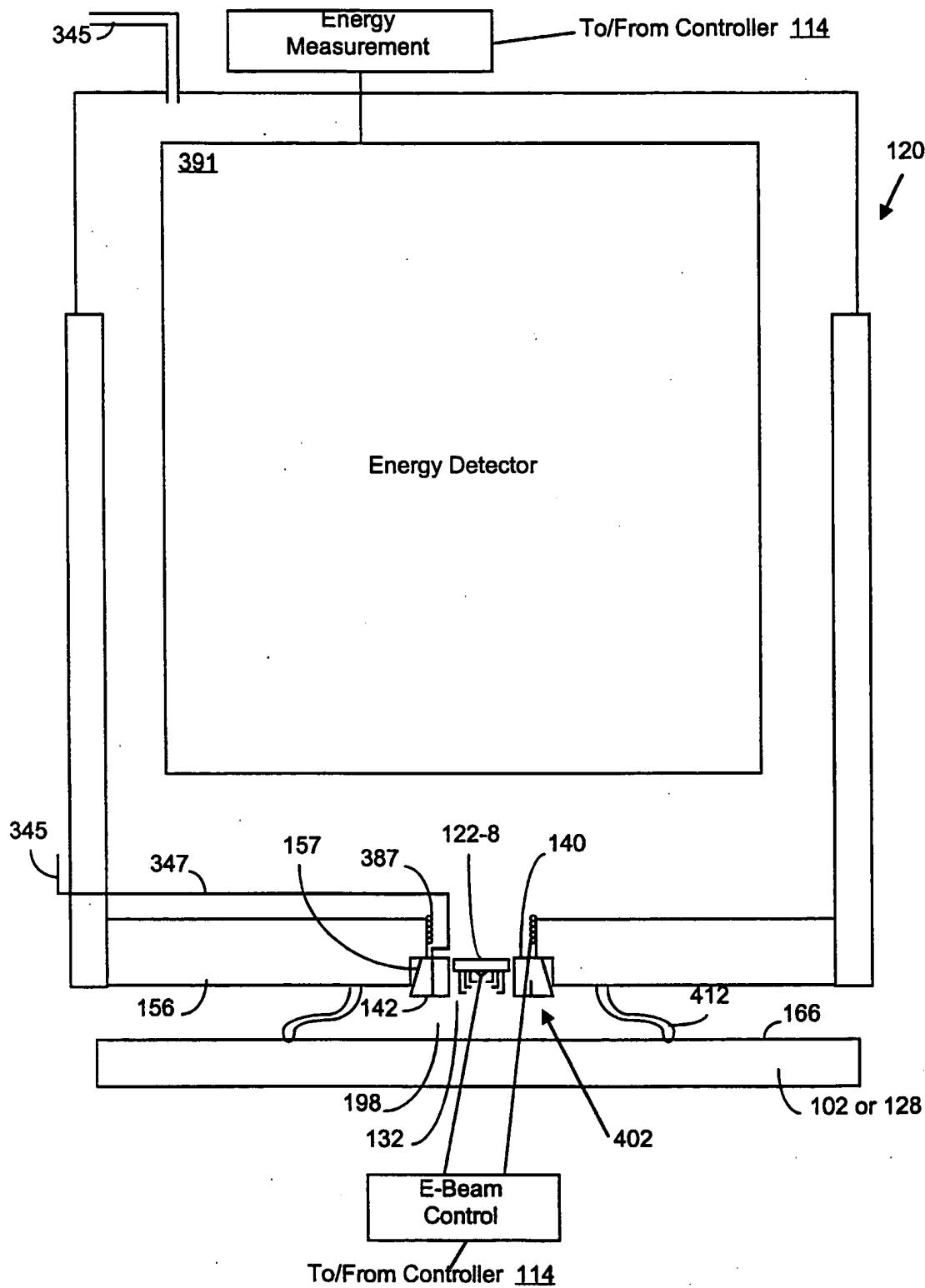


Figure 43

SUBSTITUTE SHEET (RULE 26)

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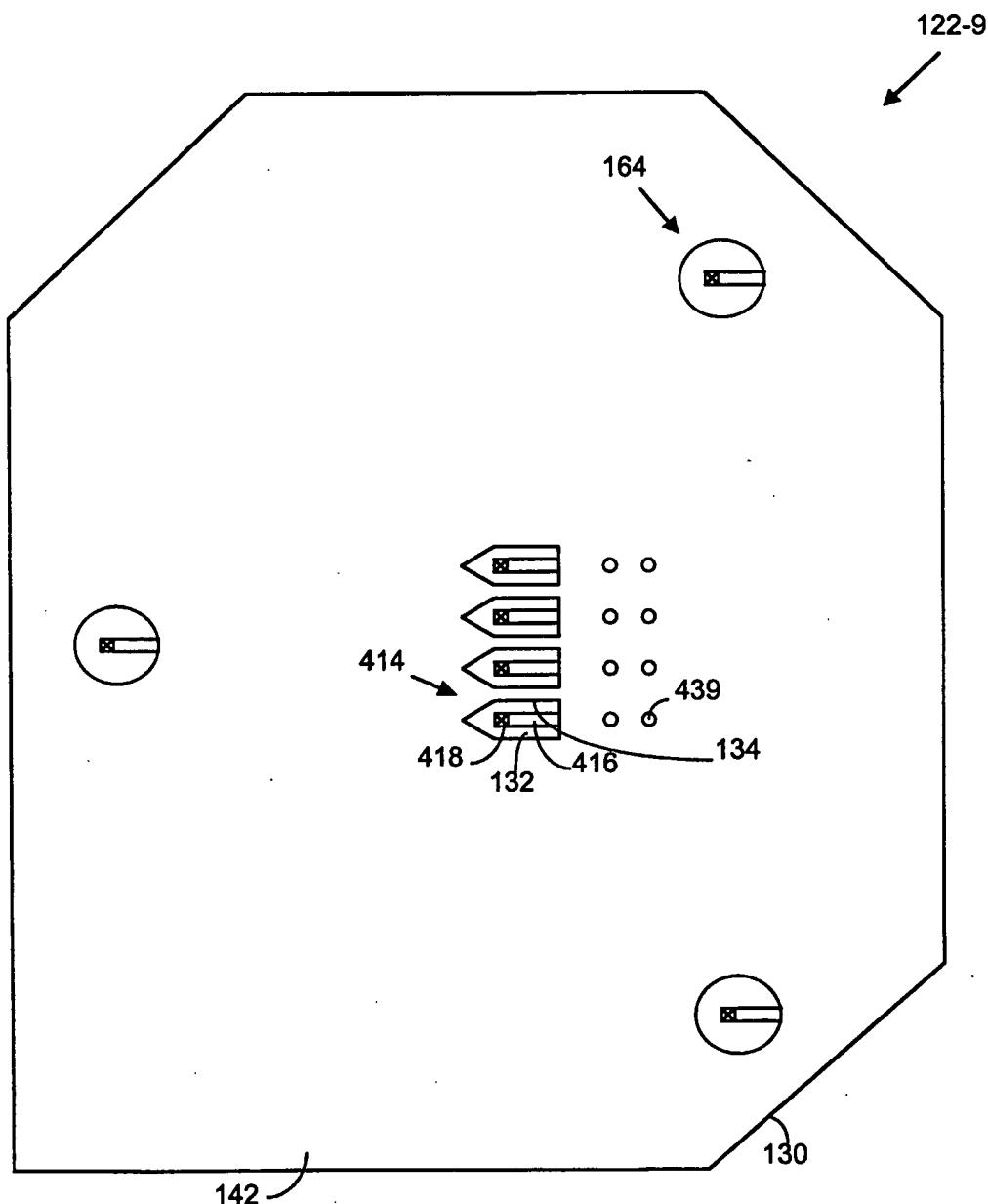


Figure 44

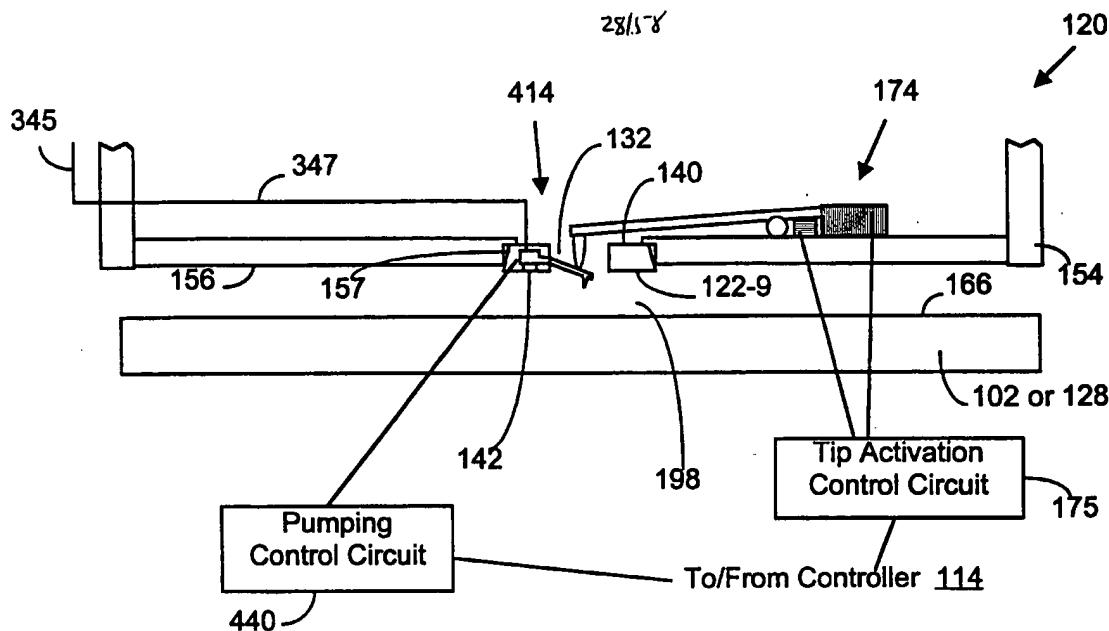


Figure 46

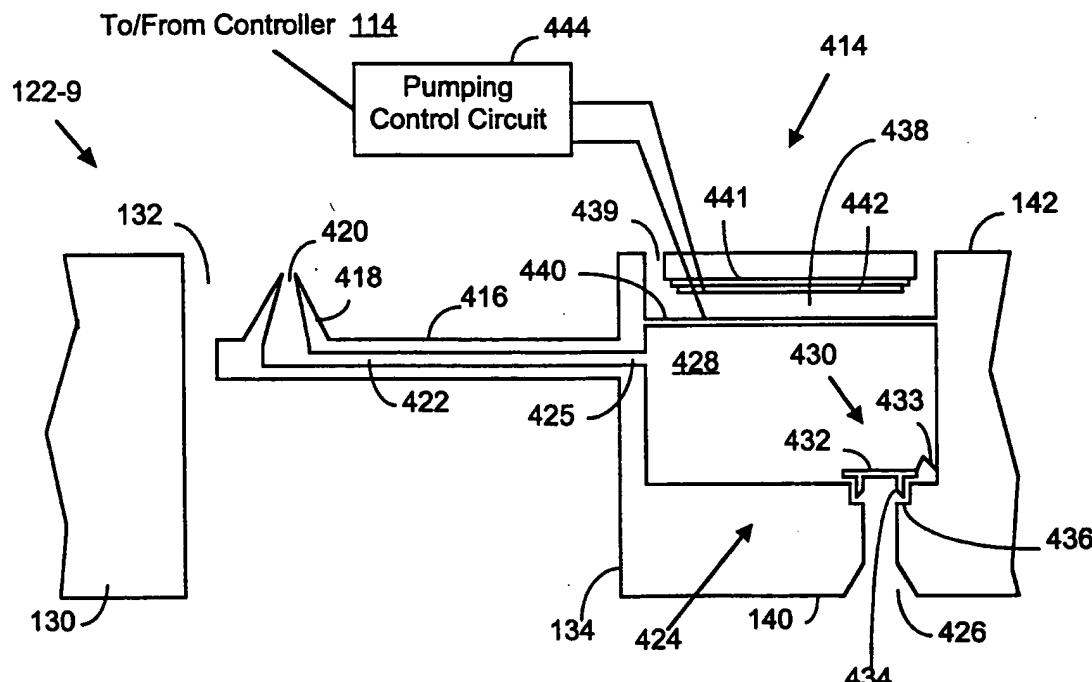


Figure 45

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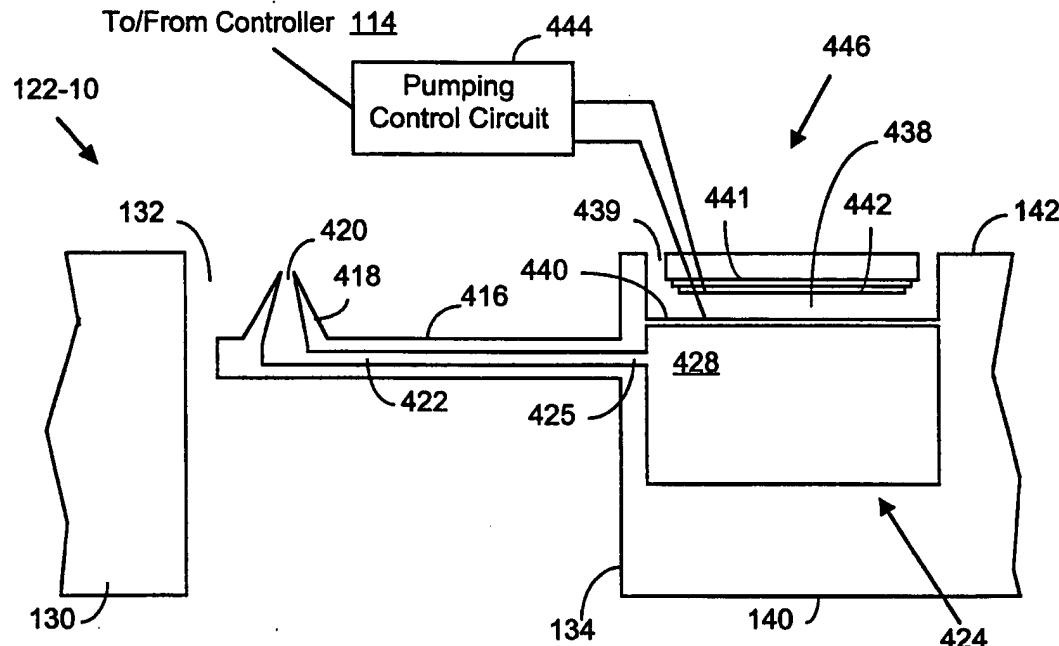


Figure 48

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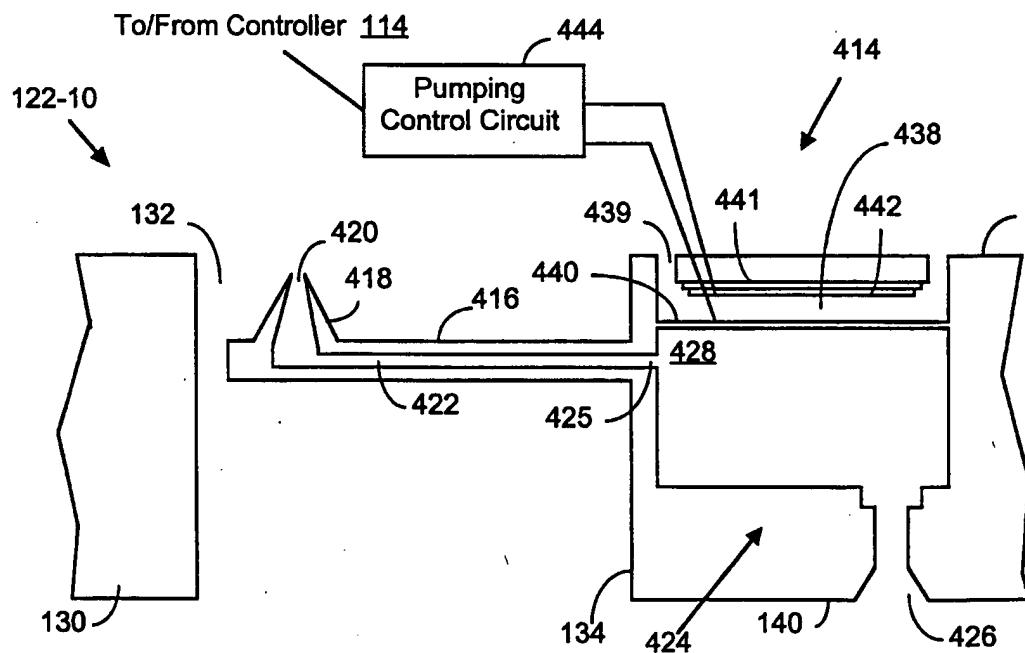


Figure 47

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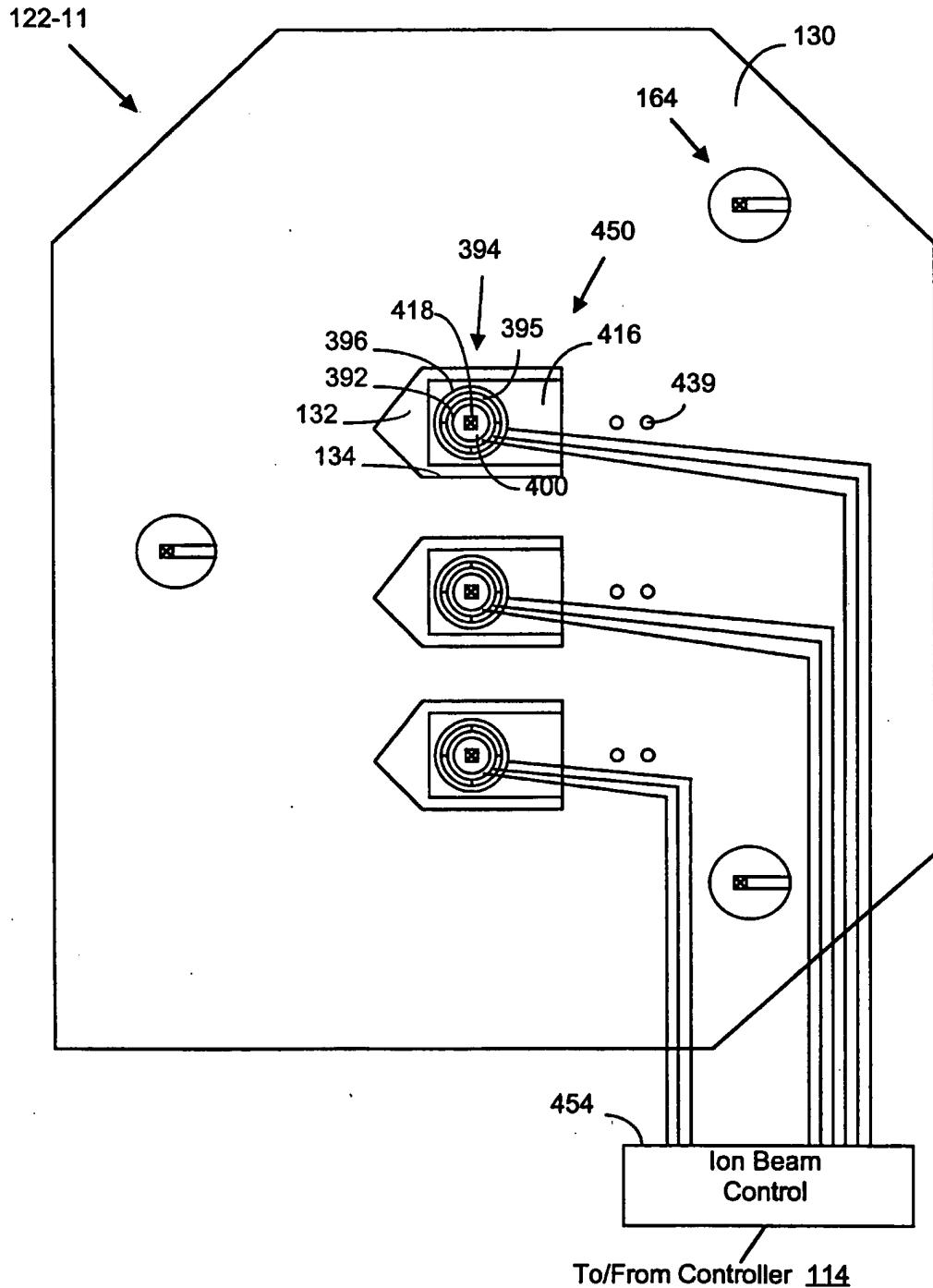


Figure 49

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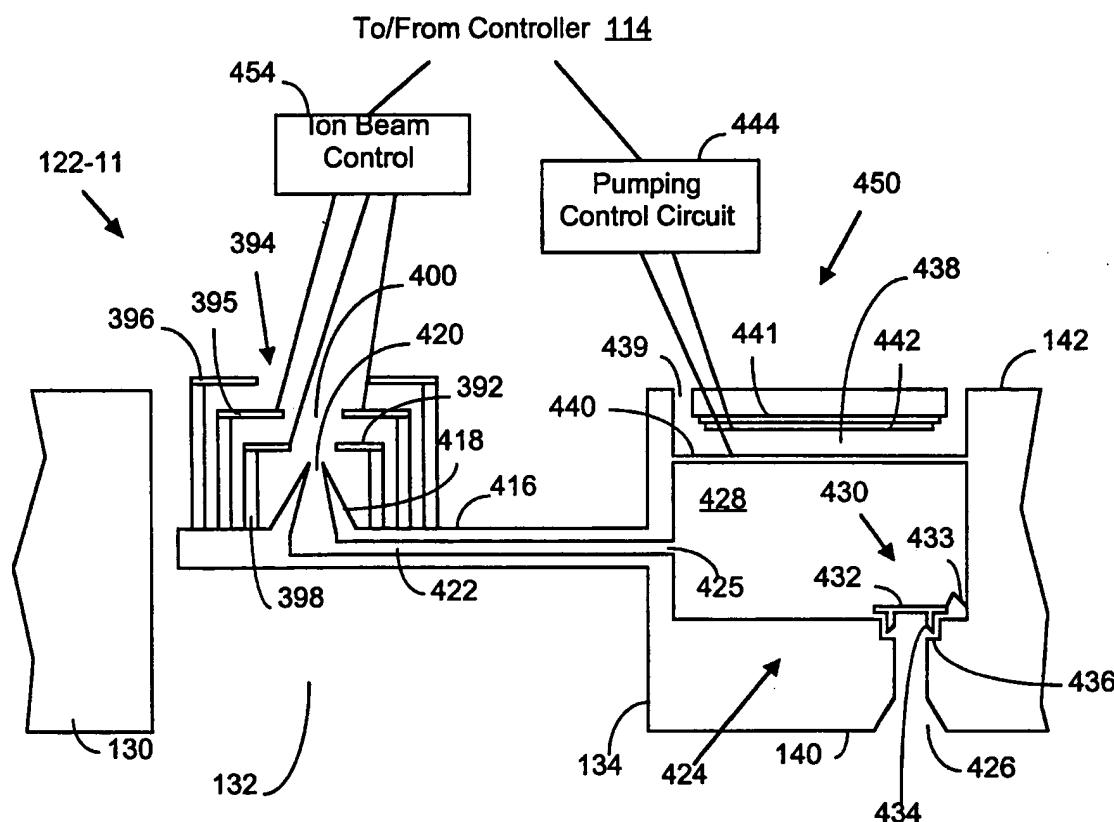


Figure 50

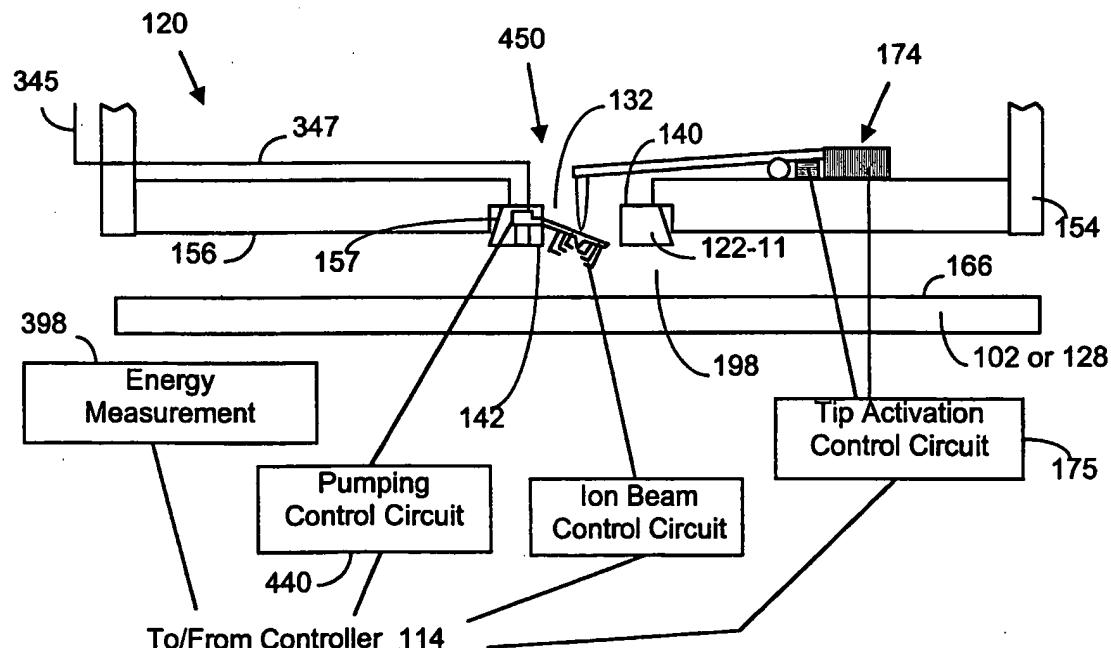


Figure 51

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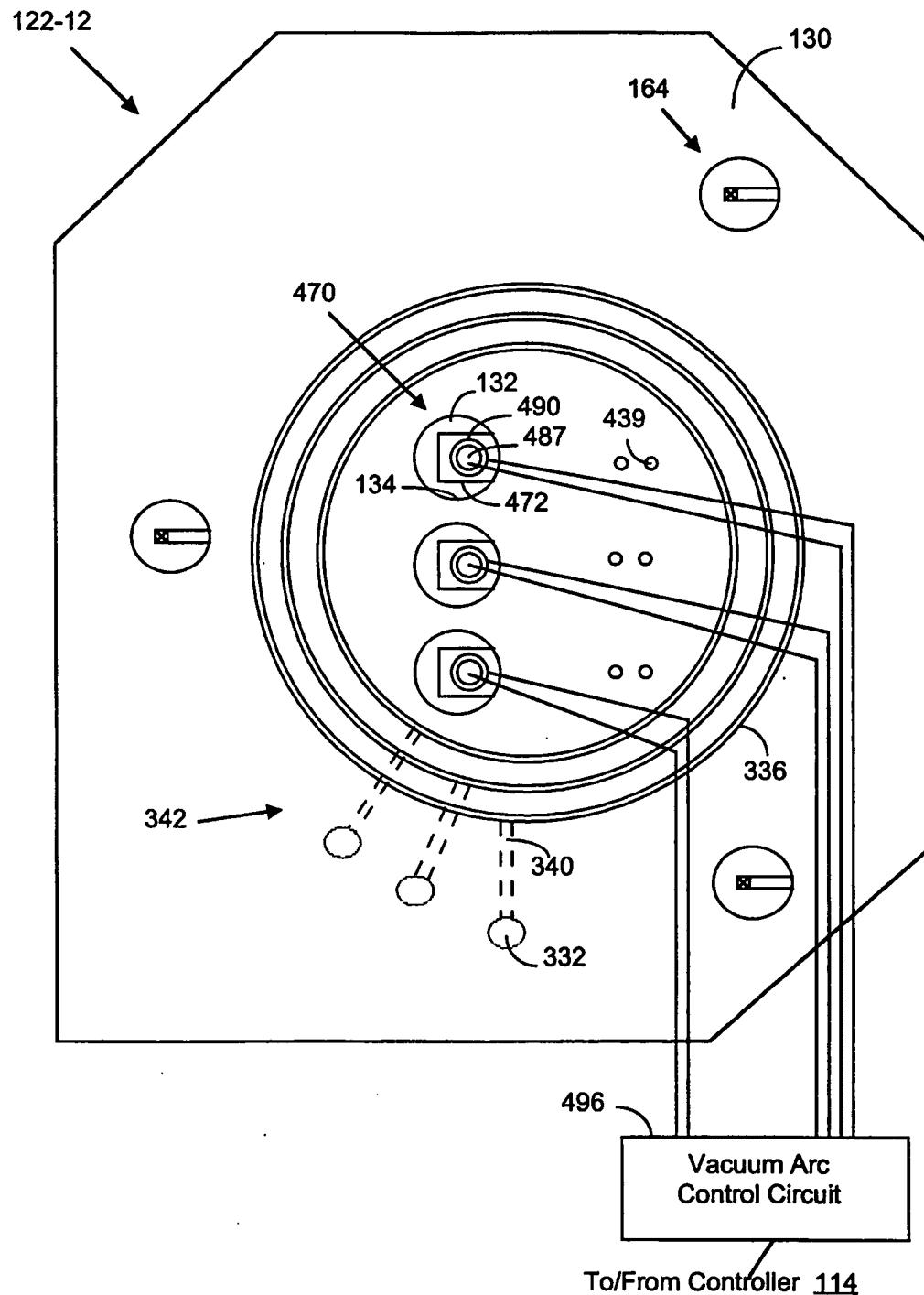


Figure 53

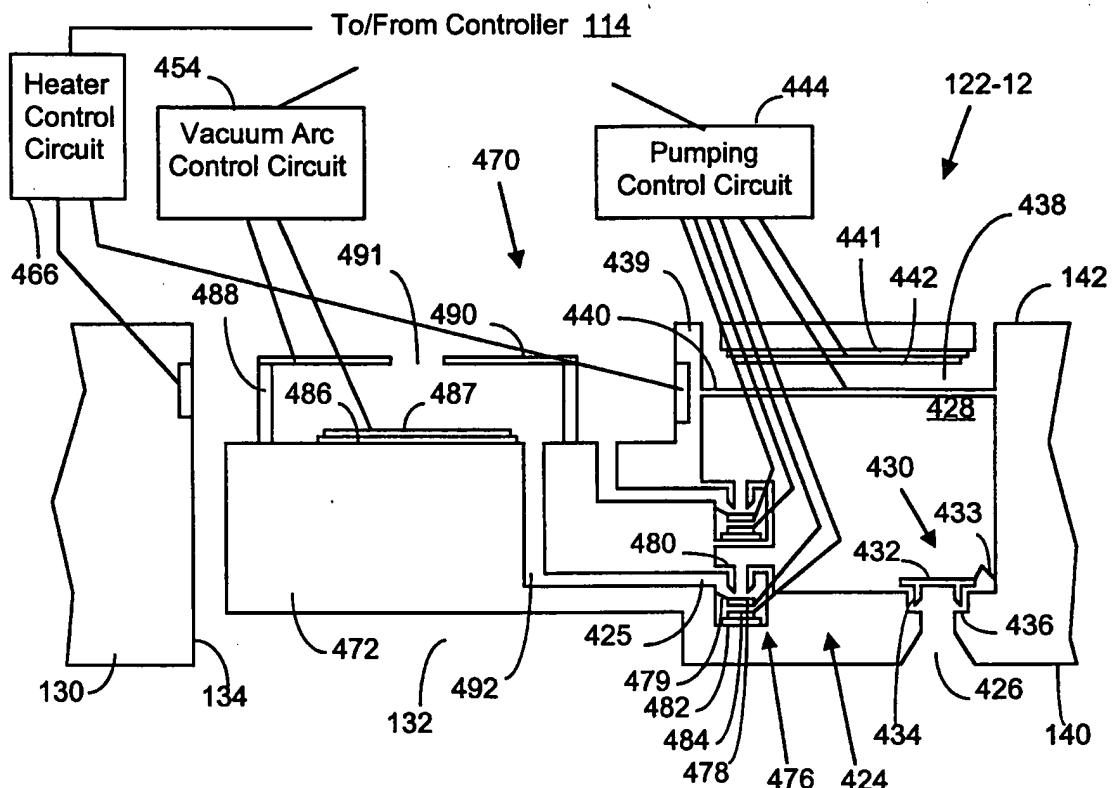


Figure 54

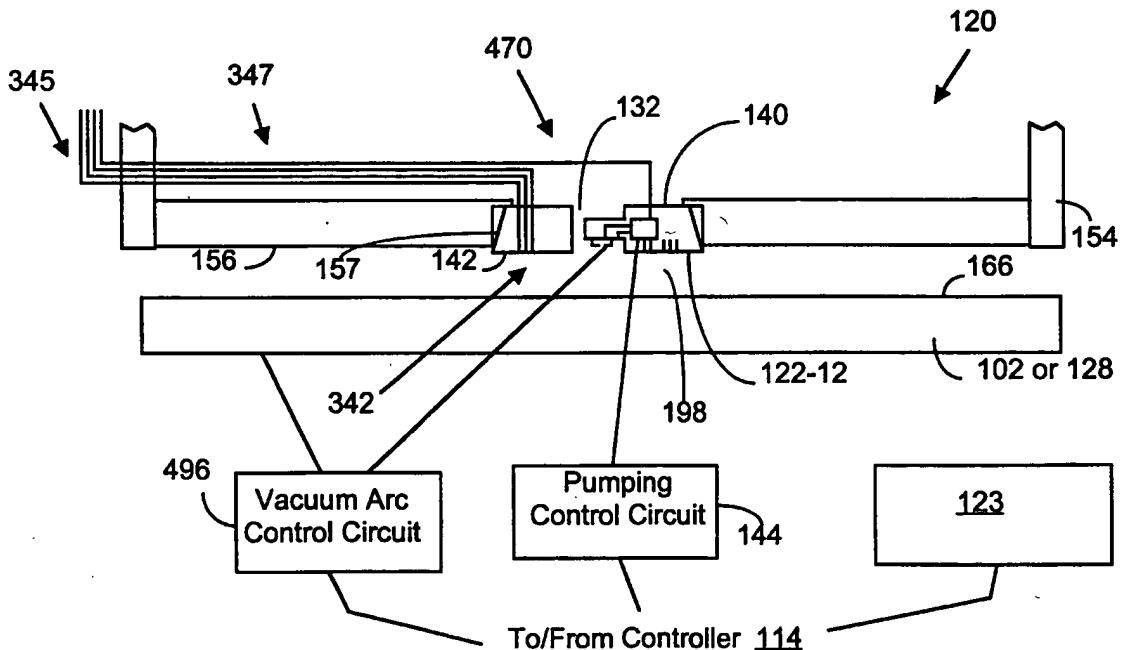


Figure 55

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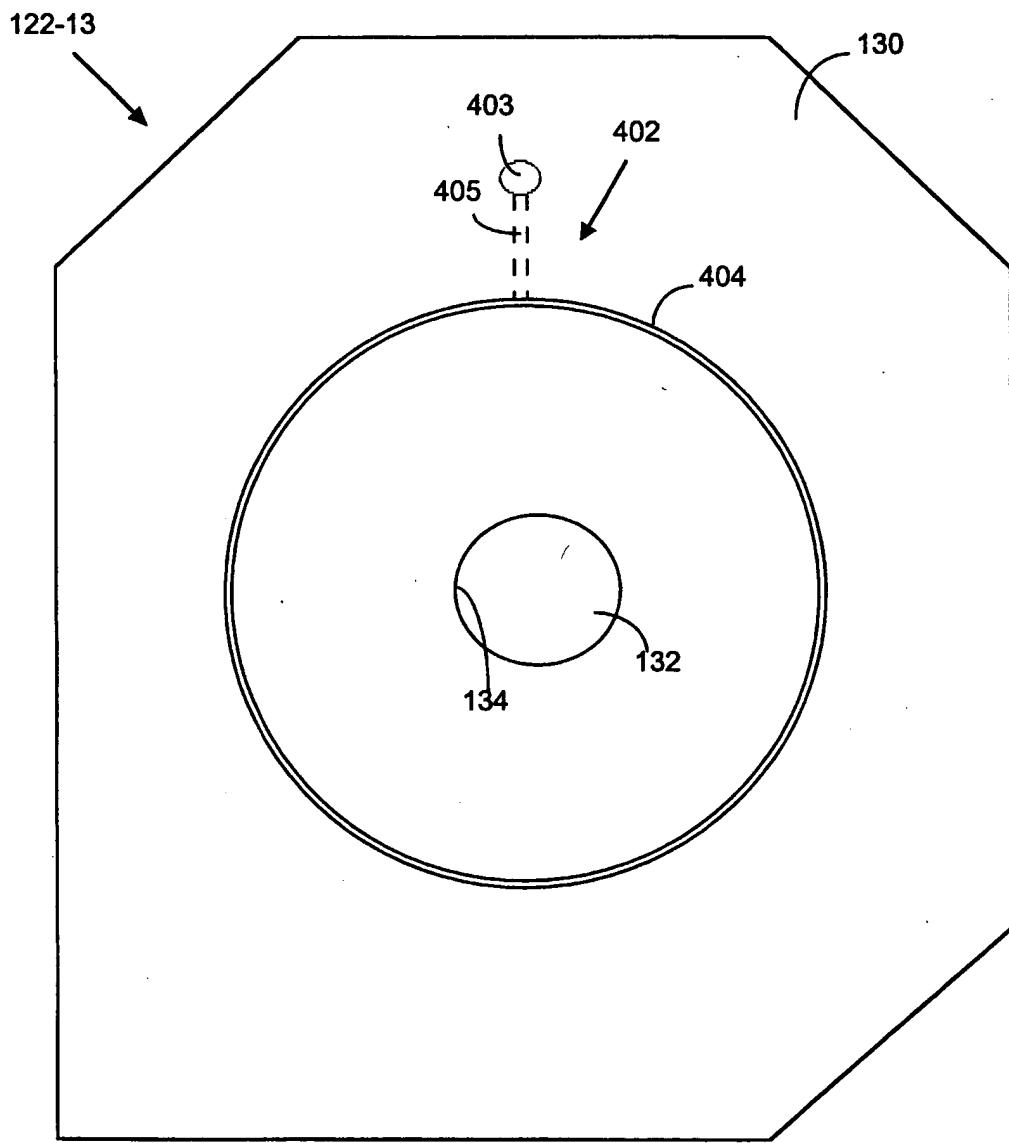


Figure 56